

PV200 HW Introduction to hardware description languages

Autumn 2015

Lector: Martin Veškna, veskrna@mail.muni.cz

Every Wednesday 16:00 – 17:40 in A415

Taught in English (Czech)

Hardware and language

- Altera DE2 development kit
- VHDL

Content of lessons

- Tutorial (common part, at the beginning)
- Exercises (individual work)

Study materials

- IS

Course completion

- Kolokvium based on the final project
- Attendance: suggested

Final project

- Several themes to choice or you can choice your own
- December 2015 – approval of the theme of the project
- January or February 2016 – defence of the project

Last common lesson

- December 2
- Last two lessons in December – project development