

Marriage Between Incompatible Couples: **Mismatched Materials**

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PHYSICS OF NEW MATERIALS

<http://www.pnm.ethz.ch>

Claudiu V. Falub



Thomas Kreiliger



Alfonso G. Taboada



Elisabeth Müller



Hans von Känel

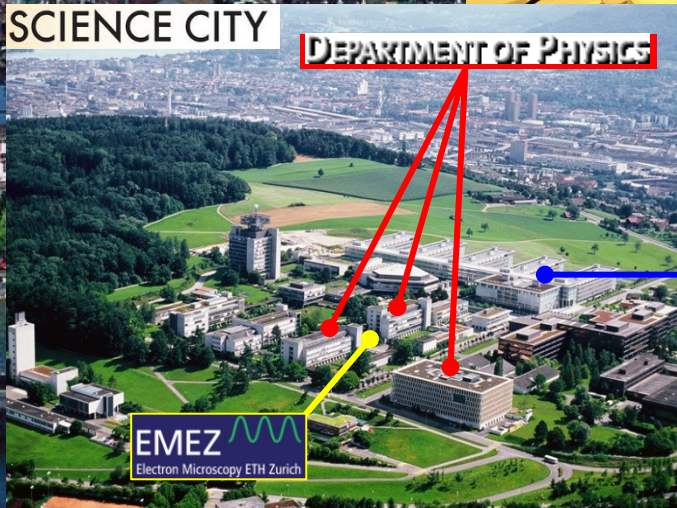


Frontiers In Research: Space and Time
860 m²

SCIENCE CITY

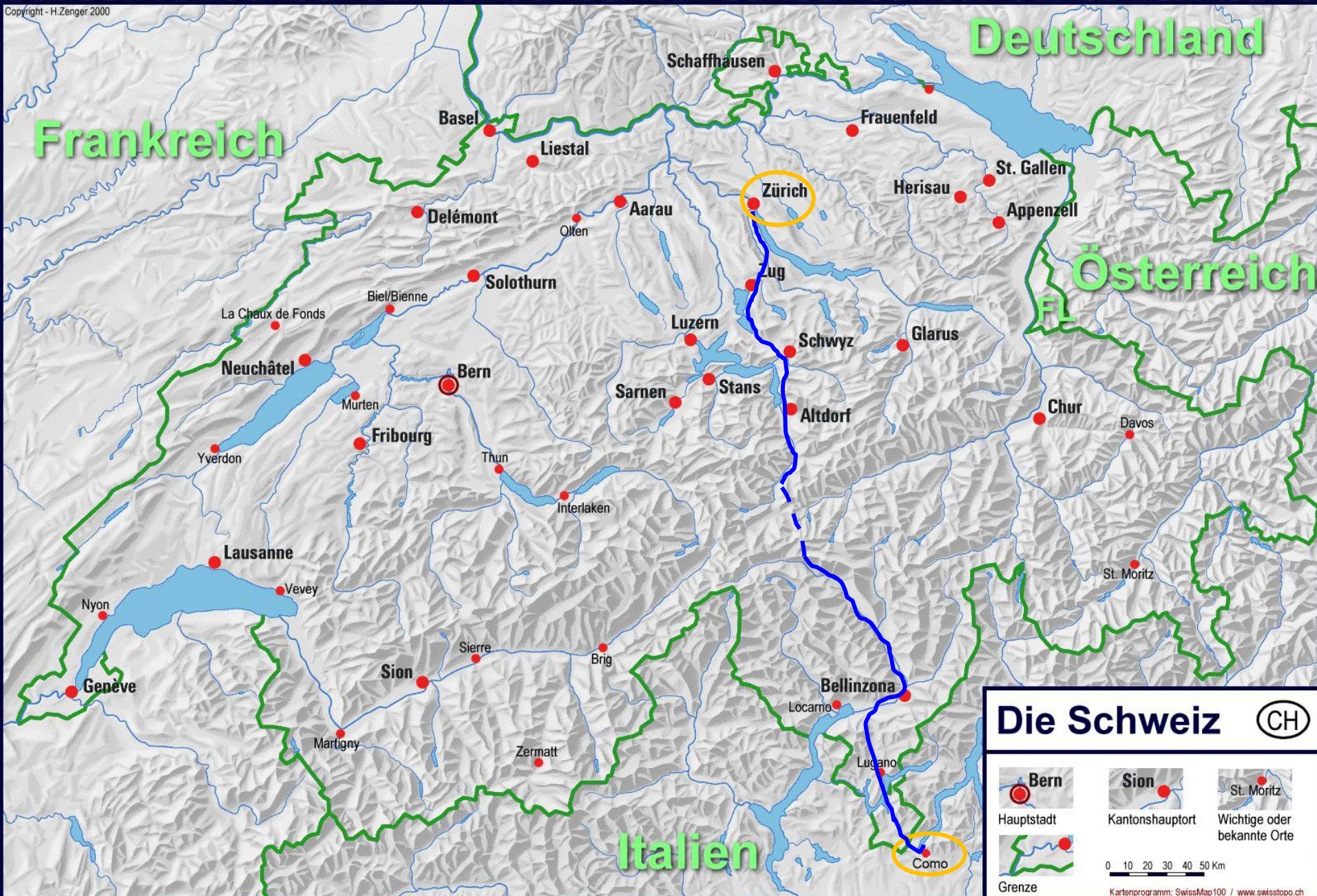
DEPARTMENT OF PHYSICS

FIRST CENTER FOR MICRO- AND NANOSCIENCE



EMEZ
Electron Microscopy ETH Zurich

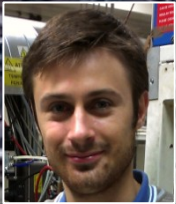




Collaborations



Fabio
Isa



Daniel
Chrastina



Giovanni
Isella



Fabio
Pezzoli



Anna
Marzegalli



Roberto
Bergamaschini



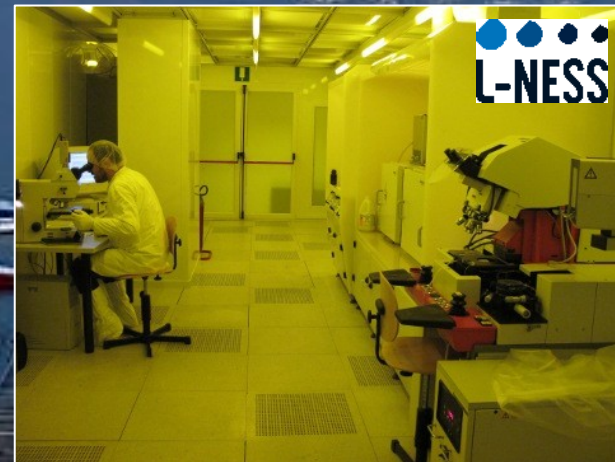
Emiliano
Bonera

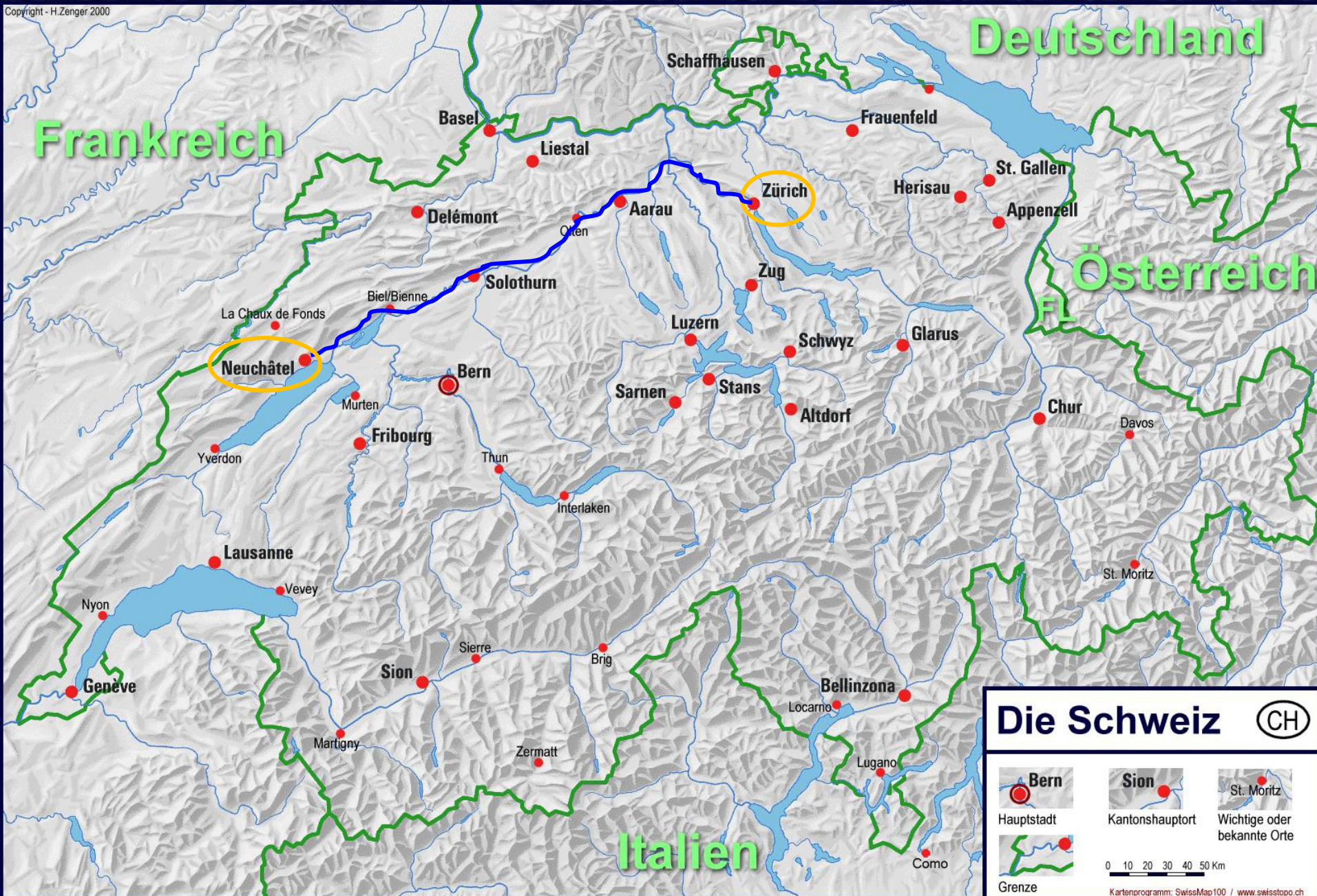


Leo
Miglio







L-NESS: Laboratory for Epitaxial Nanostructures on Silicon and Spintronics
(Como, Italy)





Die Schweiz (CH)

 Hauptstadt	 Kantonshauptort	 Wichtige oder bekannte Orte
 Grenze		

0 10 20 30 40 50 Km
Kartenprogramm: SwissMap100 / www.swisstopo.ch



**Philippe
Niedermann**



**Antonia
Neels**



**Aurélie
Pezous**



**Rolf
Kaufmann**



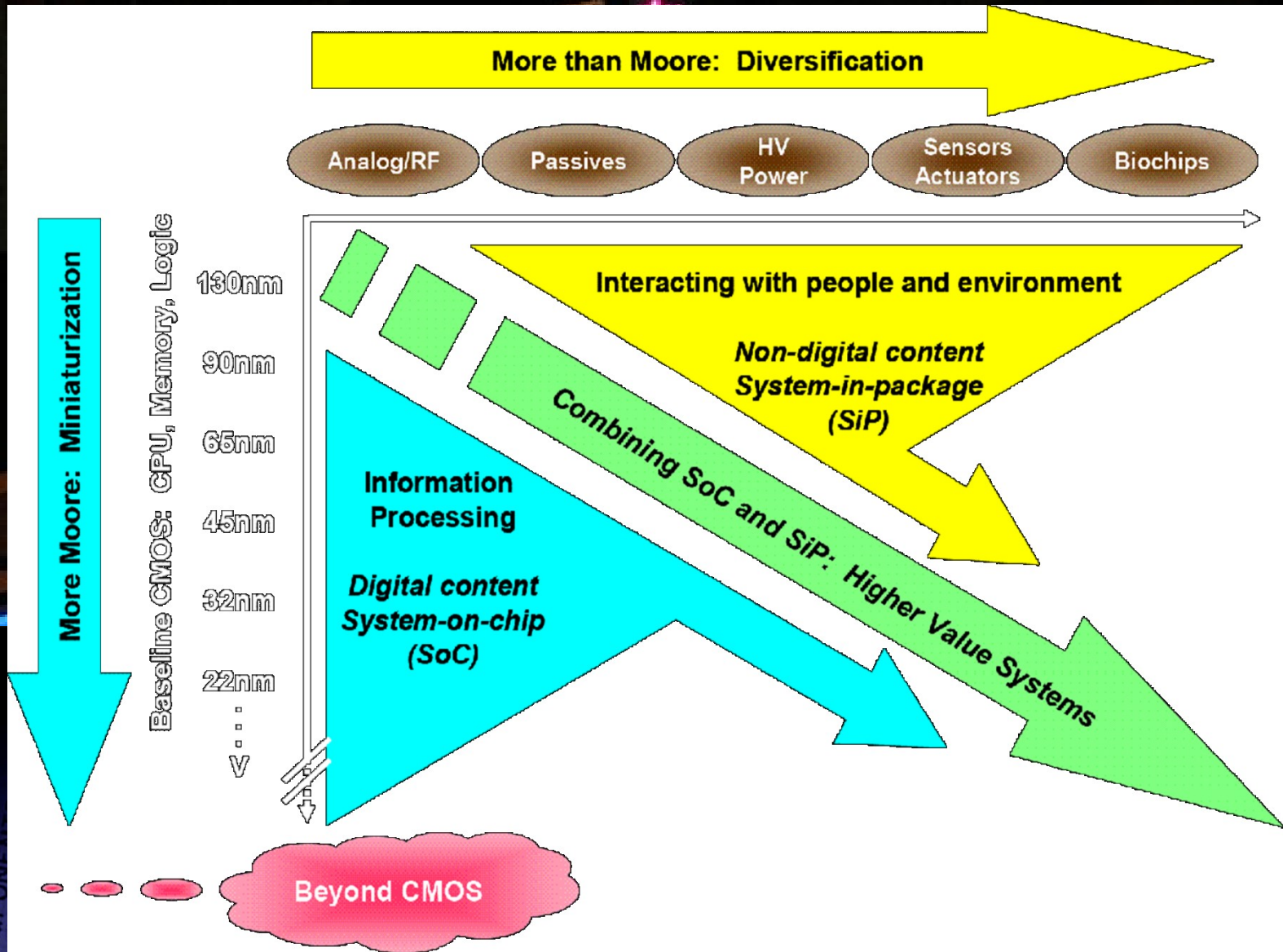
**Alex
Dommann**



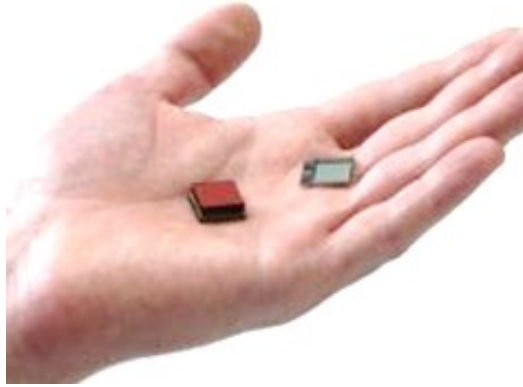
**Mojmir
Meduňa**



International Roadmap for Semiconductors



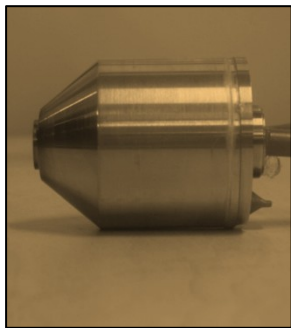
Motivation: Integrated Miniaturized X-ray Systems



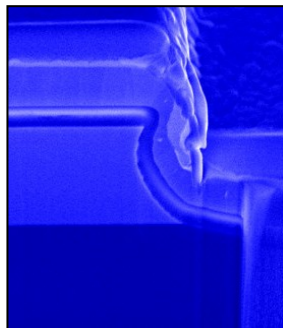
“NEXRAY”

- **Next Generation X-Ray** Systems
- High resolution/sensitivity
- Ge as conversion layer
- No bump-bonding (**monolithic integration**)

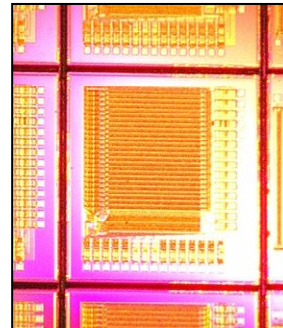
Fast, programmable
X-ray sources



Ge layers for high-
energy X-ray detection



Single-photon solid-
state X-ray detection

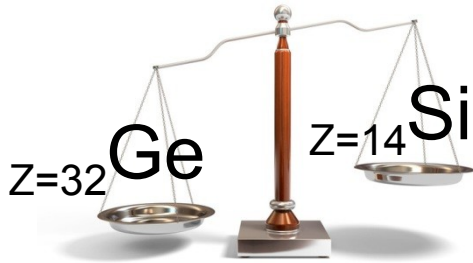


Phase contrast
X-ray imaging

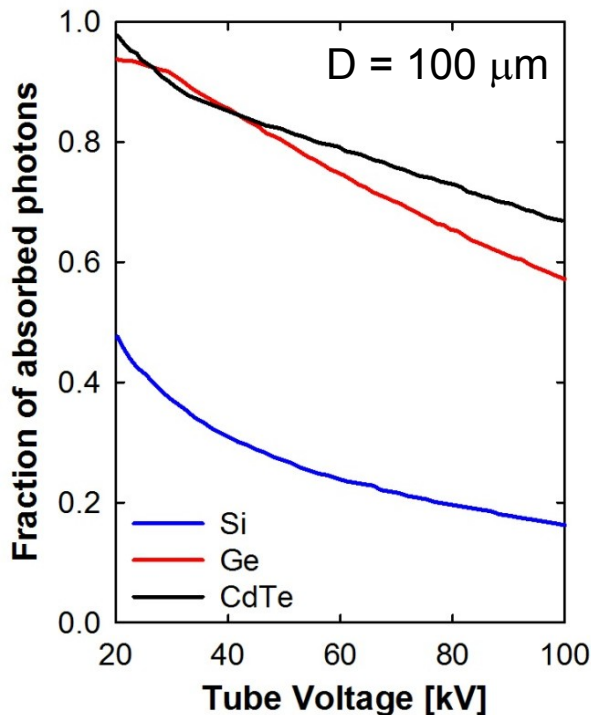
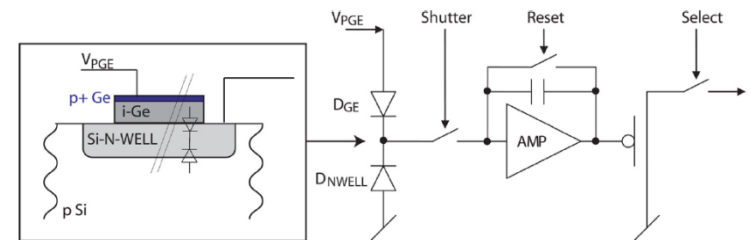
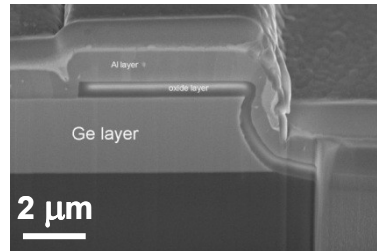


Motivation: Next Generation X-Ray Detectors

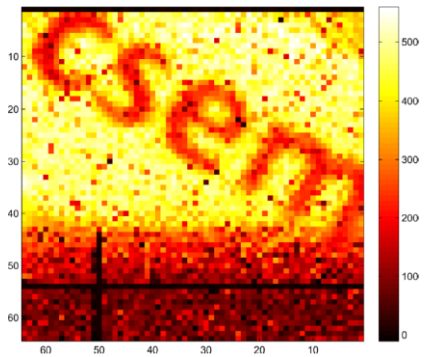
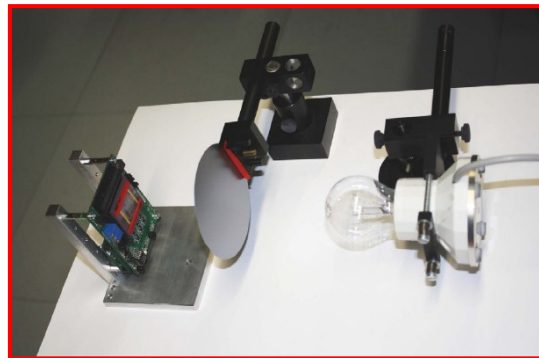
- Why Germanium?



- Monolithic integration of a 3 μm Ge film with CMOS for IR radiation was demonstrated at ETHZ/CSEM



64×64 pixel IR sensor with integrated Ge photodiodes

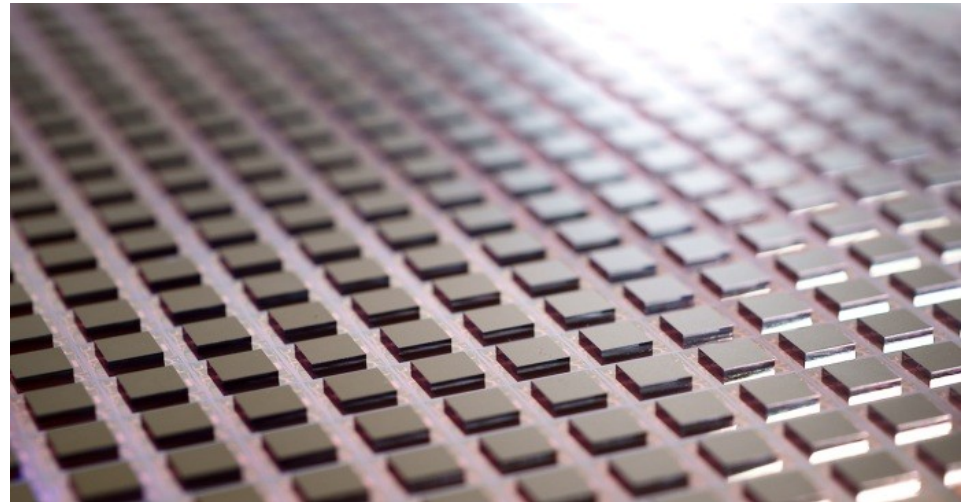
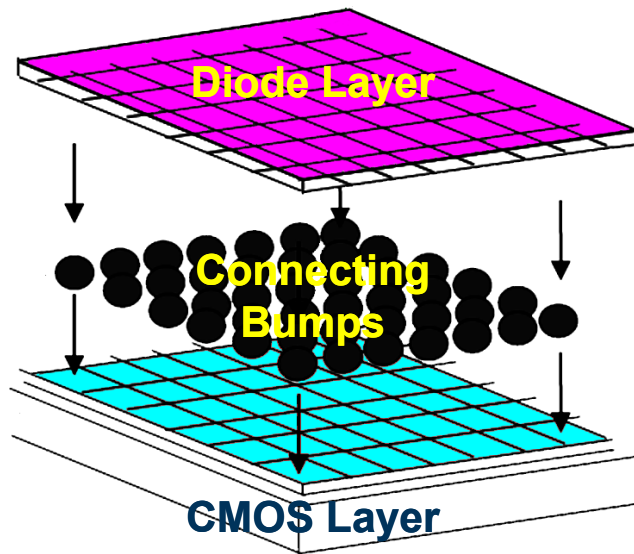


R. Kaufmann *et al.*, J. Appl. Phys. **110**, 023107 (2011)

For X-rays → **SUPER THICK (> 50 μm !!!)** high quality (i.e. dislocations, uniformity) Ge epilayers

Integration of Mismatched Materials: **State-of-the-art**

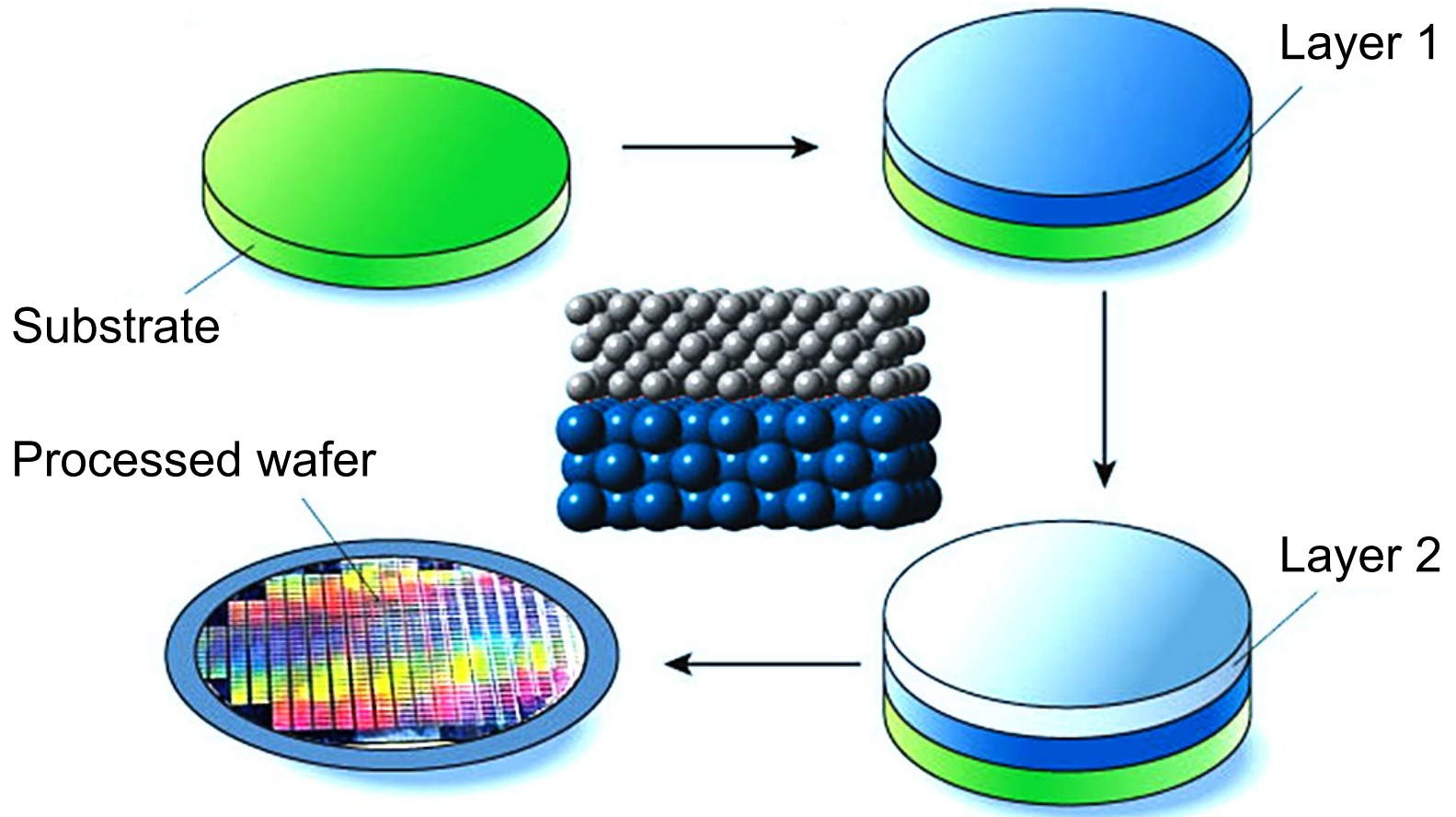
A. Wafer Bonding



- ⊗ Debonding may occur at large ΔT
- ⊗ Expensive bump-bonding required

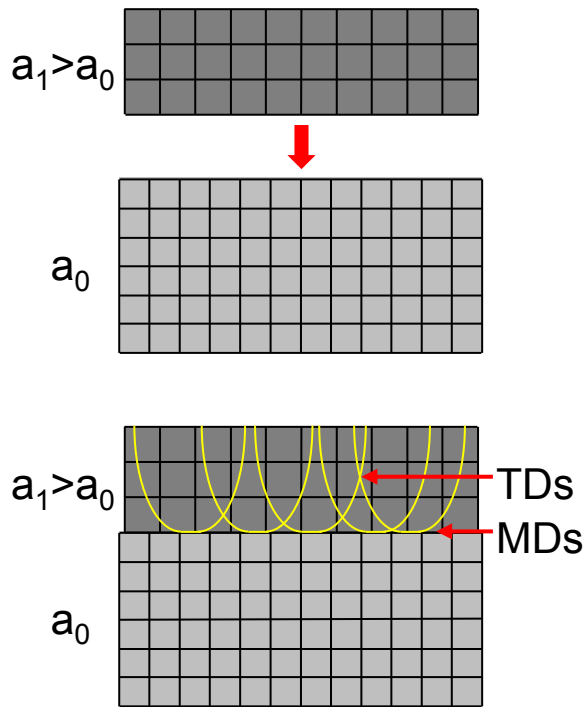
Integration of Mismatched Materials: **State-of-the-art**

B. Monolithic Integration (HETERO-EPITAXY)

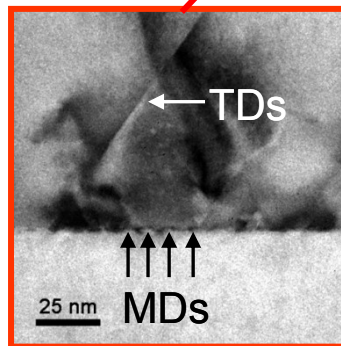
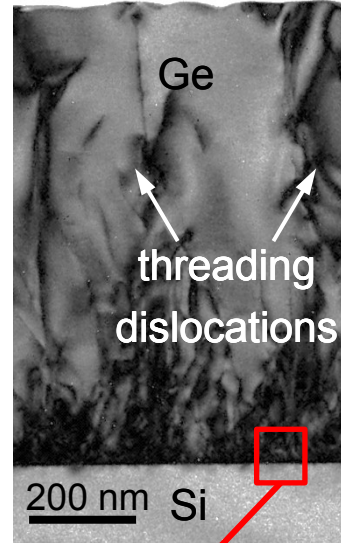


Key Problems of Hetero-Epitaxy

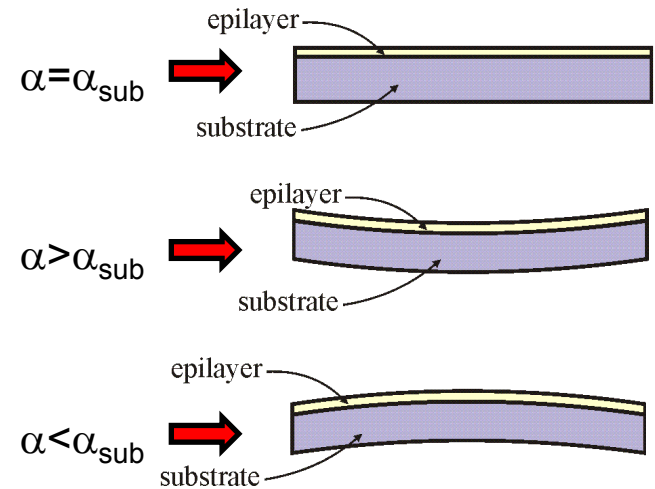
Dislocations (lattice mismatch)



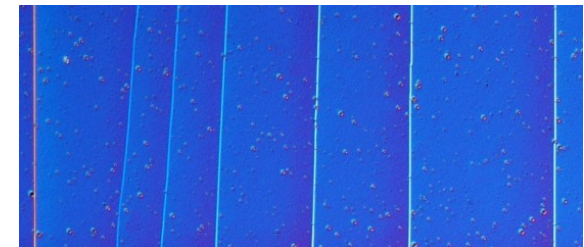
TDs – threading dislocations
MDs – misfit dislocations



Wafer Bowing & Cracks (thermal mismatch)



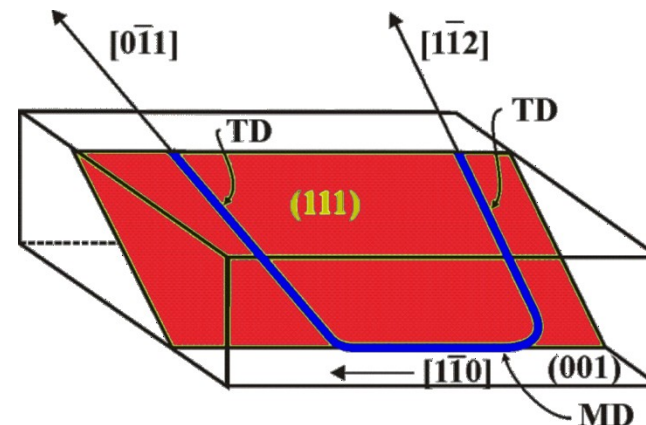
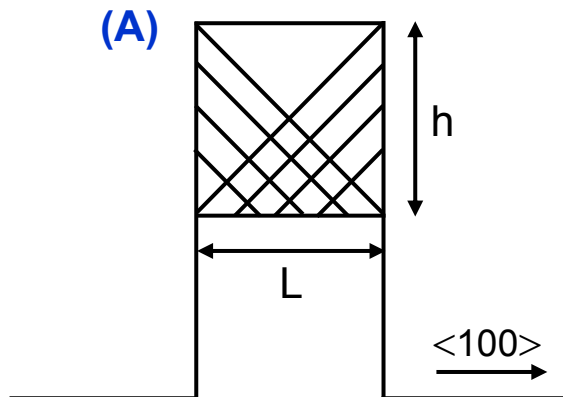
Nomarski top view micrograph



30 μm Ge/Si(001)

Dislocations Control by Substrate Patterning

- S. Luryi, E. Suhir, APL **49**, 140 (1986) → critical thickness increases very much with the reduction of the lateral dimension of the patterned areas
- (A) ▪ E.A. Fitzgerald, N. Chand, J. Electron. Mater. **20**, 839 (1991) → Epitaxial Necking (GaAs on Si)
- (B) ▪ T.A. Langdo et al., APL **76**, 3700 (2000) → Epitaxial Necking (Ge on Si)
- (C) ▪ J.S. Park et al., APL **90**, 052113 (2007) → Aspect Ratio Trapping (ART)



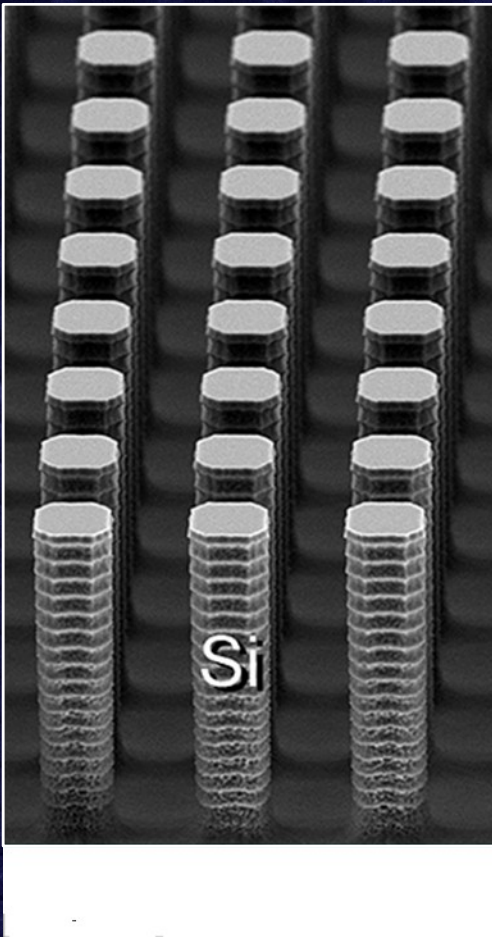
60-degree dislocations

A.E. Blakeslee, MRS Symp. Proc. **148**, 217 (1989)

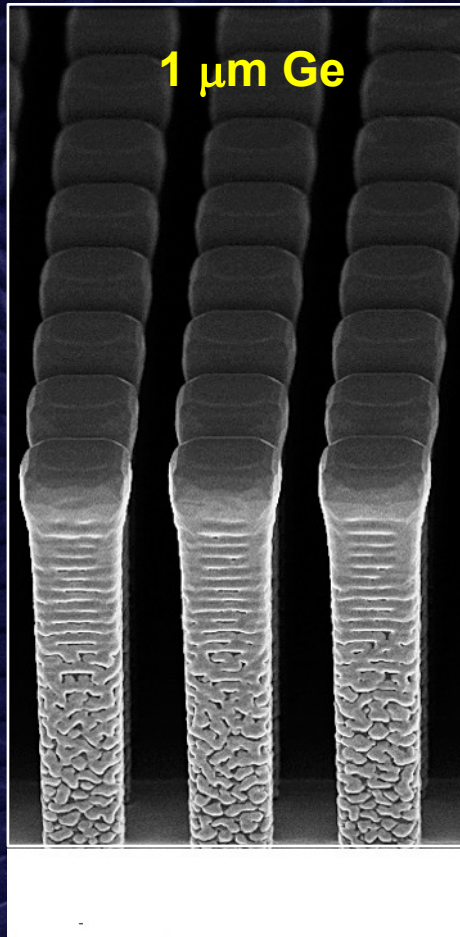
J.W. Matthews et al., JAP **41**, 3800 (1970)

Growth on Patterned Si Wafers

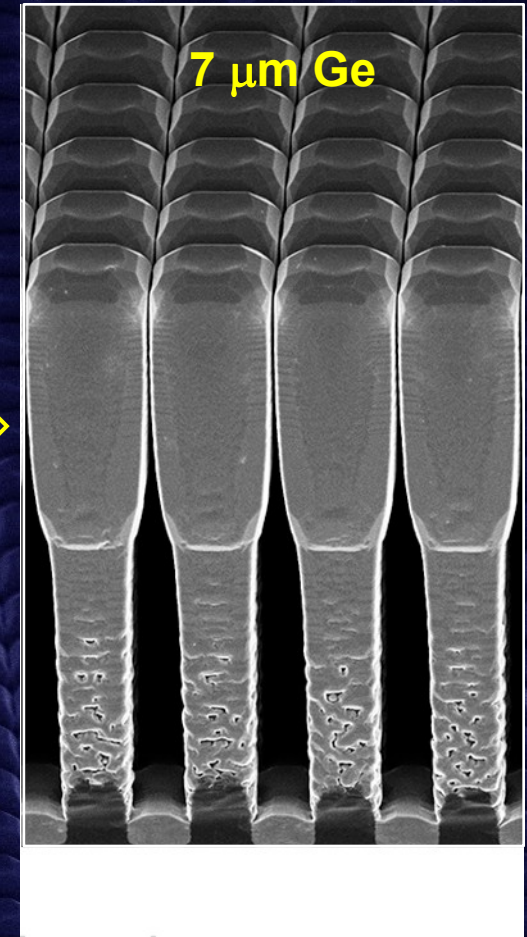
Si “pillars”
(DRIE)



Ge growth (~4 nm/s)
(LEPECVD)

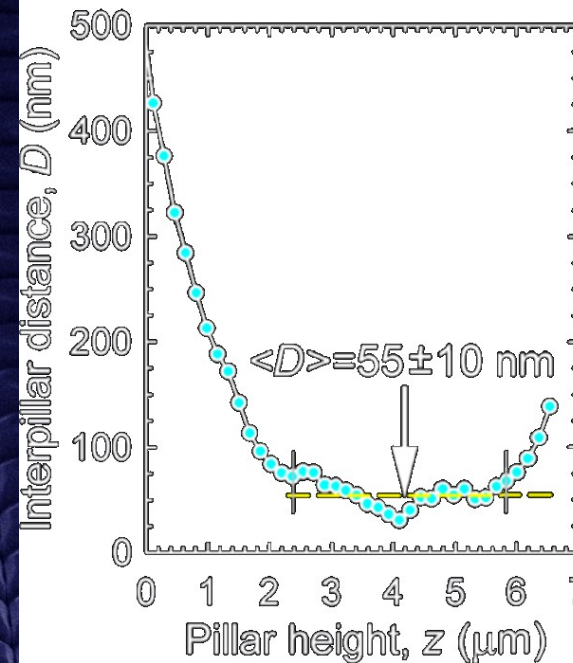
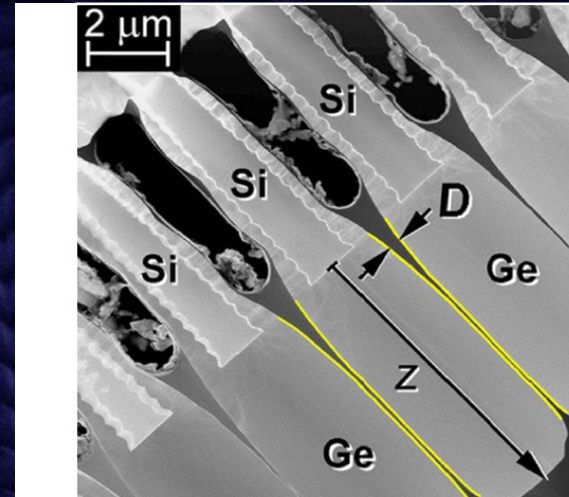
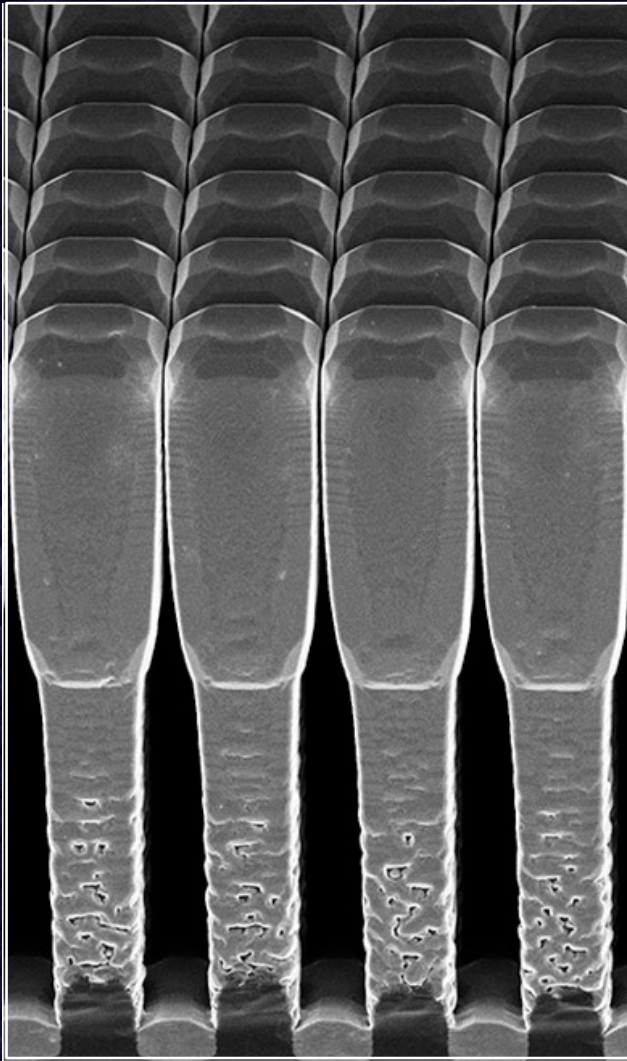


Ge “towers”
(Quenched Lateral Growth)



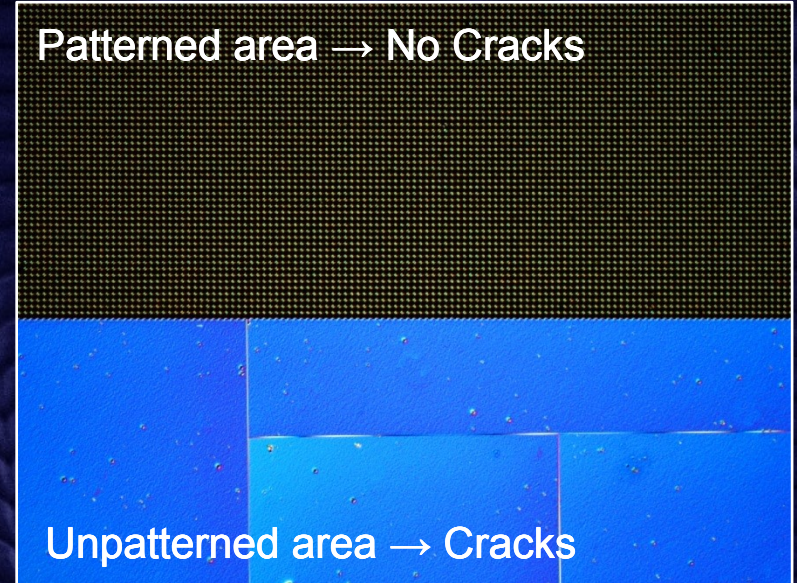
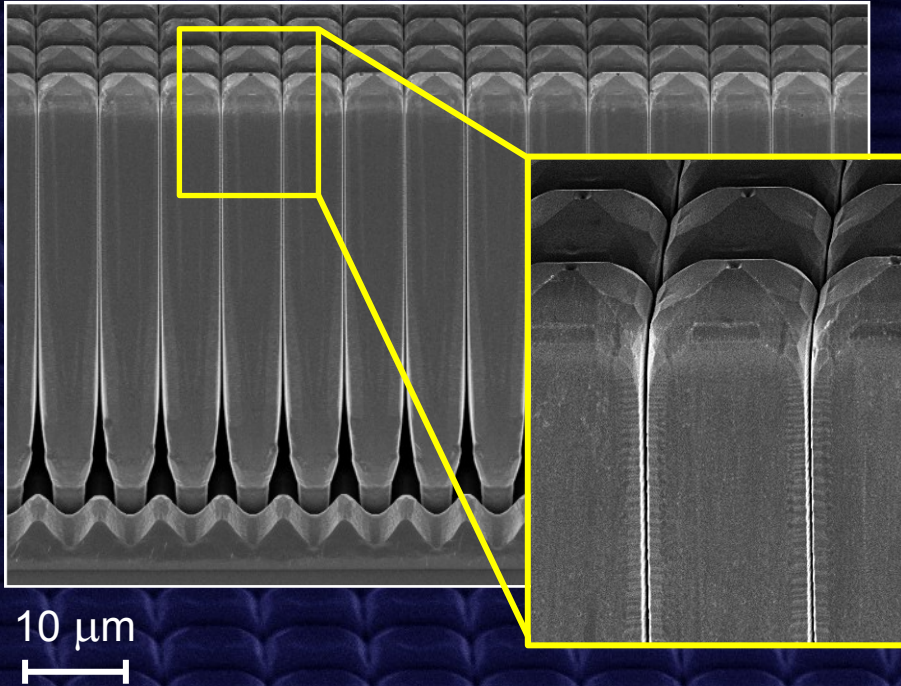
LEPECVD: “Low Energy Plasma Enhanced CVD” (H. von Känel, ETH-Zürich)

New Growth Mechanism: Self Limiting Lateral Growth



Unlimited Layer Thickness

Ge "towers": 50 μm (!)



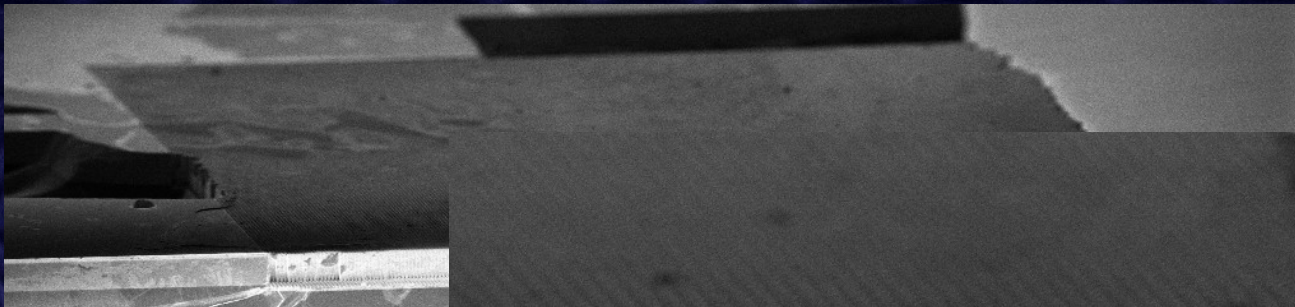
100 μm thin pre-patterned 8" CMOS wafer completely covered with 20- μm -tall Ge towers → **very small bowing!**



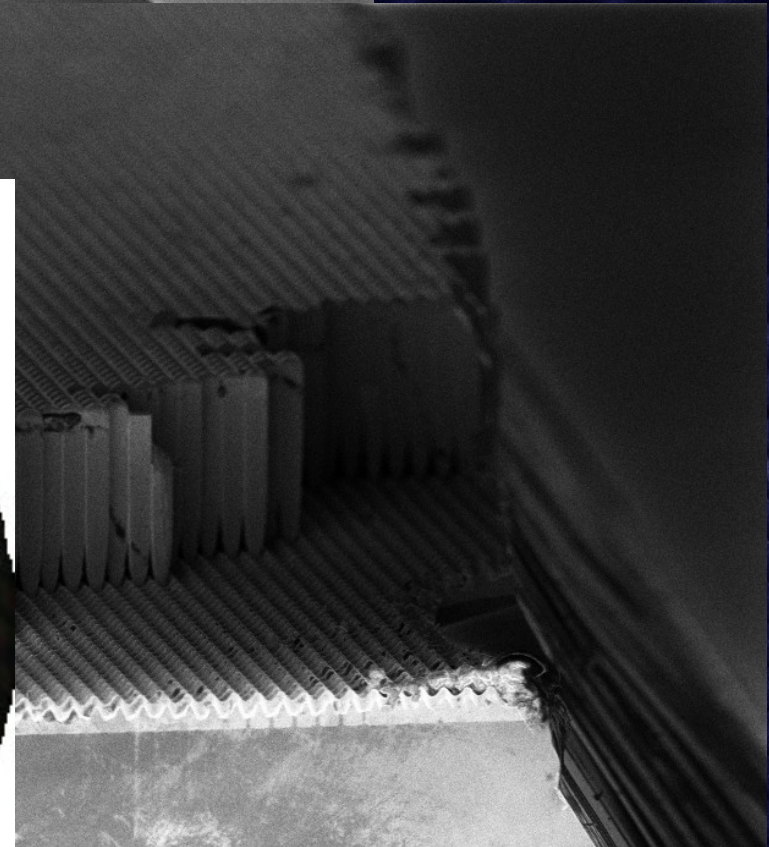
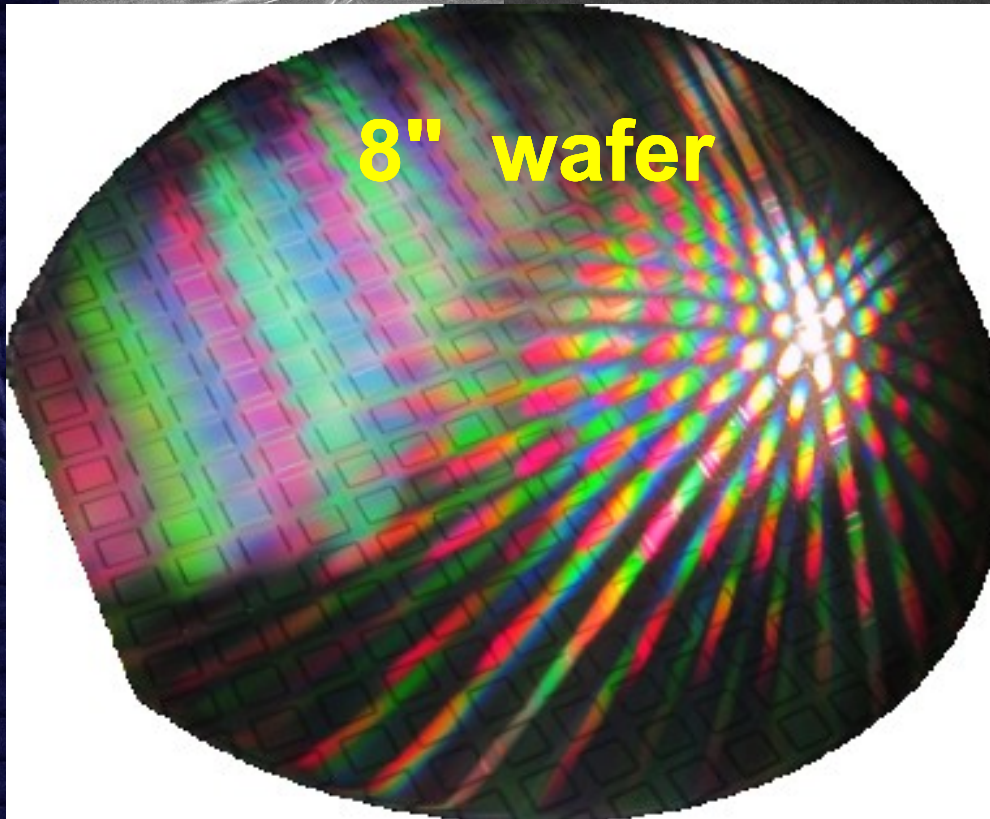
- Cracks don't propagate into the patterned area
- Elimination of the wafer bowing

Very High Surface Filling

Ge "towers": 50 μm (!)



8" wafer

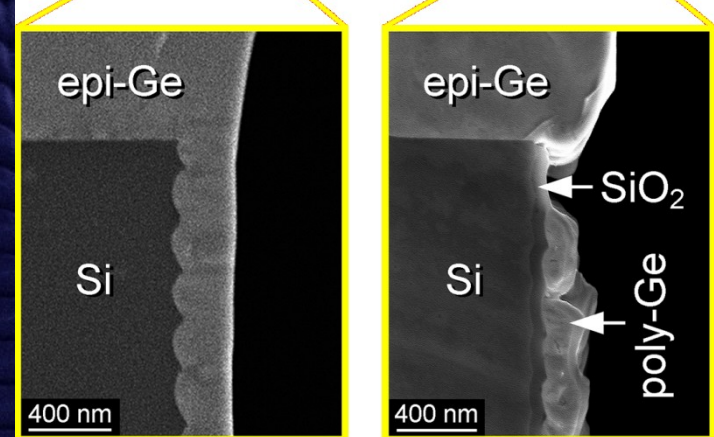
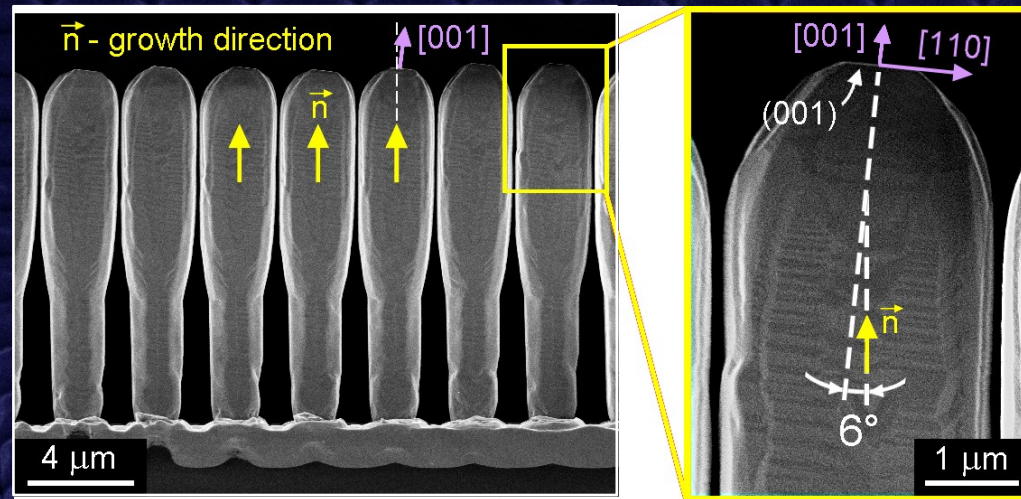
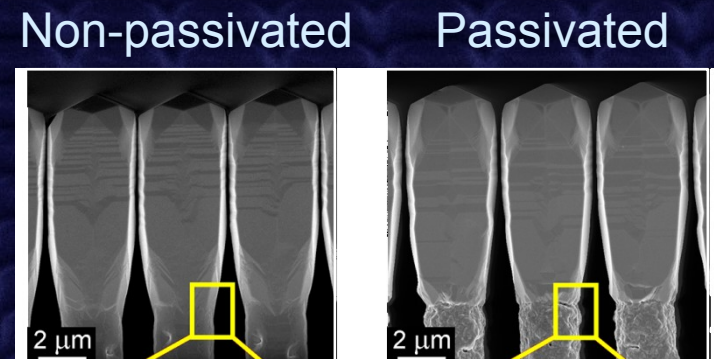
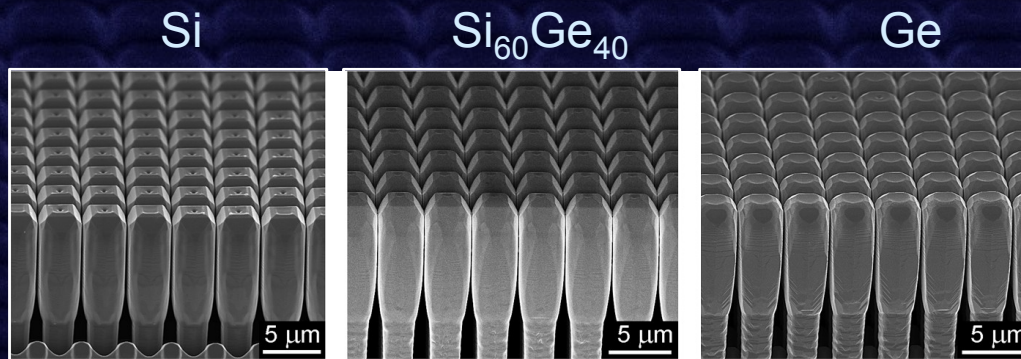


Signal A = InLens
Mag = 357 X

Date :10 Sep 2012
Time :13:11:22

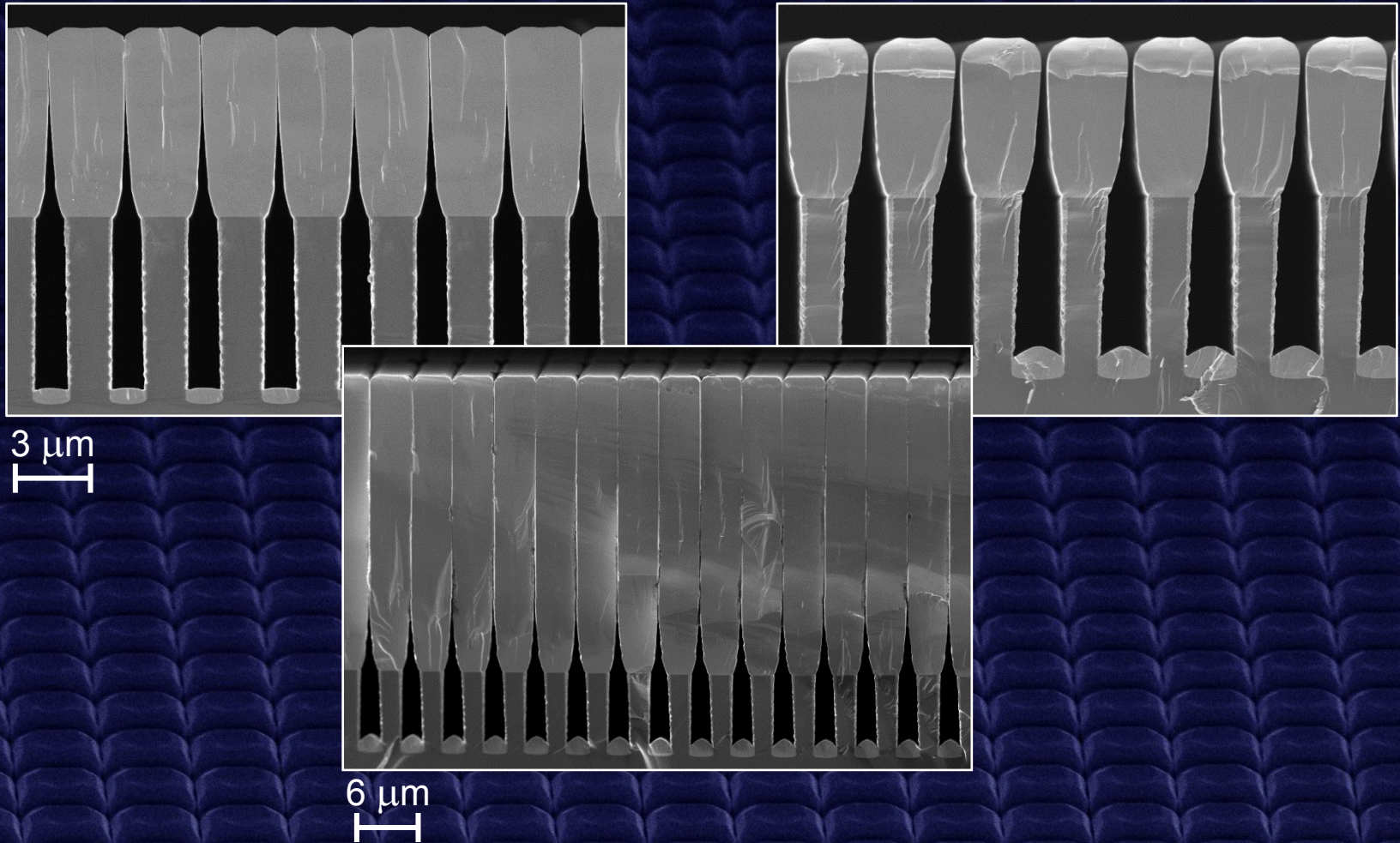


Ubiquity of the Epitaxial Growth Mode



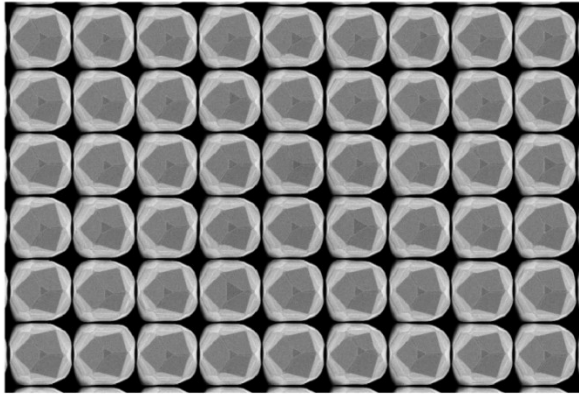
Ge towers are obtained for different thermal/lattice mismatch, substrate miscut, geometry of the patterns (e.g. pillars, ridges), initial facet distribution (passivated or not), smoothness of the pillar sidewalls !

Growth of Germanium on Si Ridges

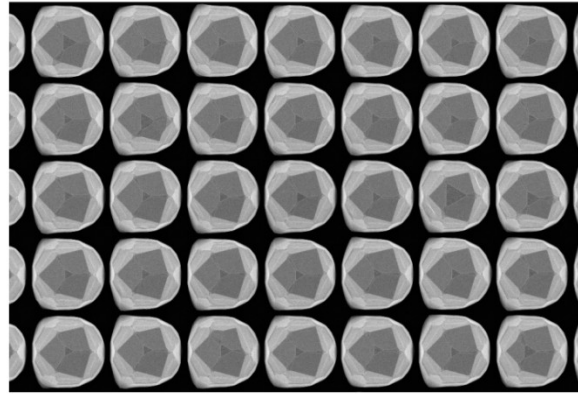


⇒ Growth of Ge on Si ridges is similar to that on Si pillars

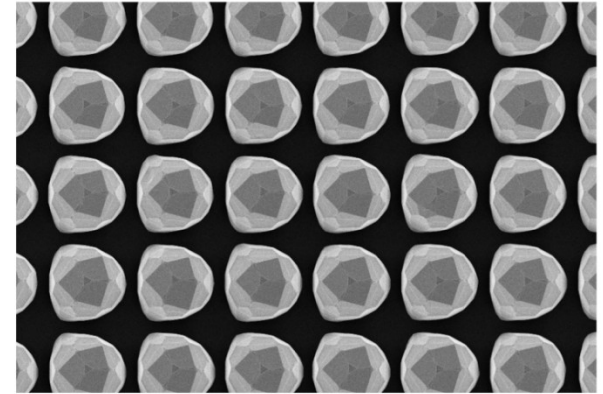
Growth of Germanium on Si(111) substrates



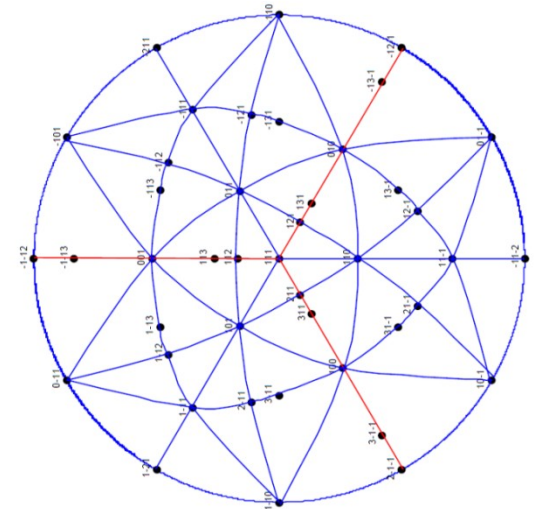
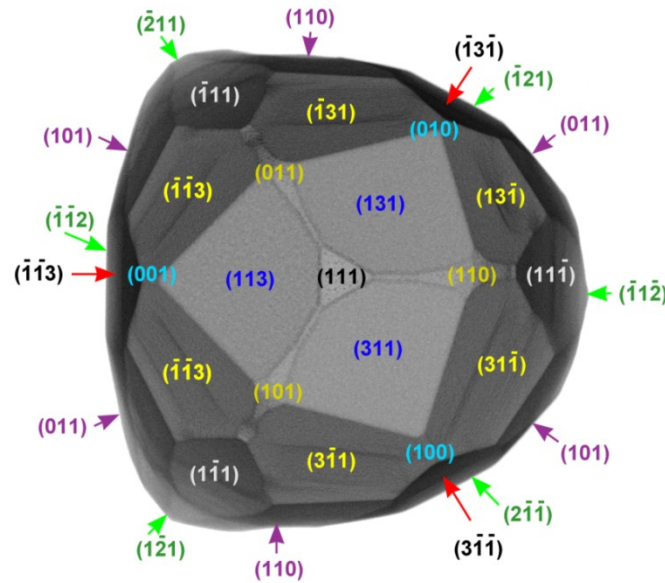
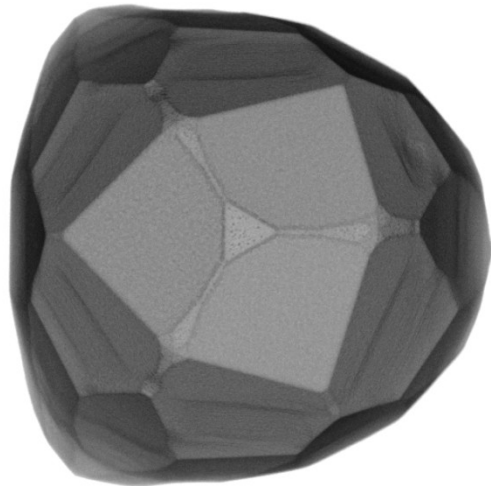
2 μm



2 μm



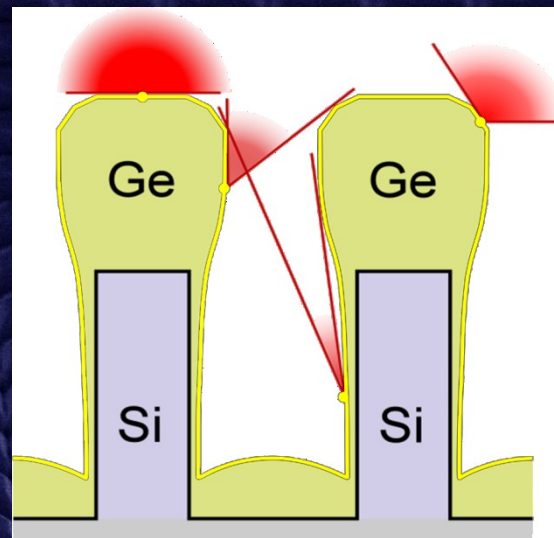
2 μm



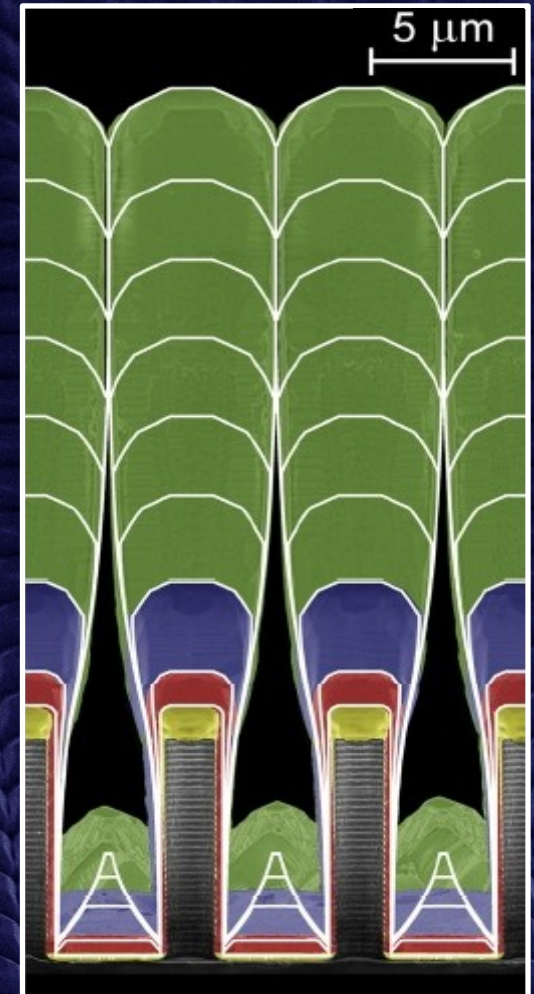
From Isolated Pillars to Closely Spaced Pillar Array



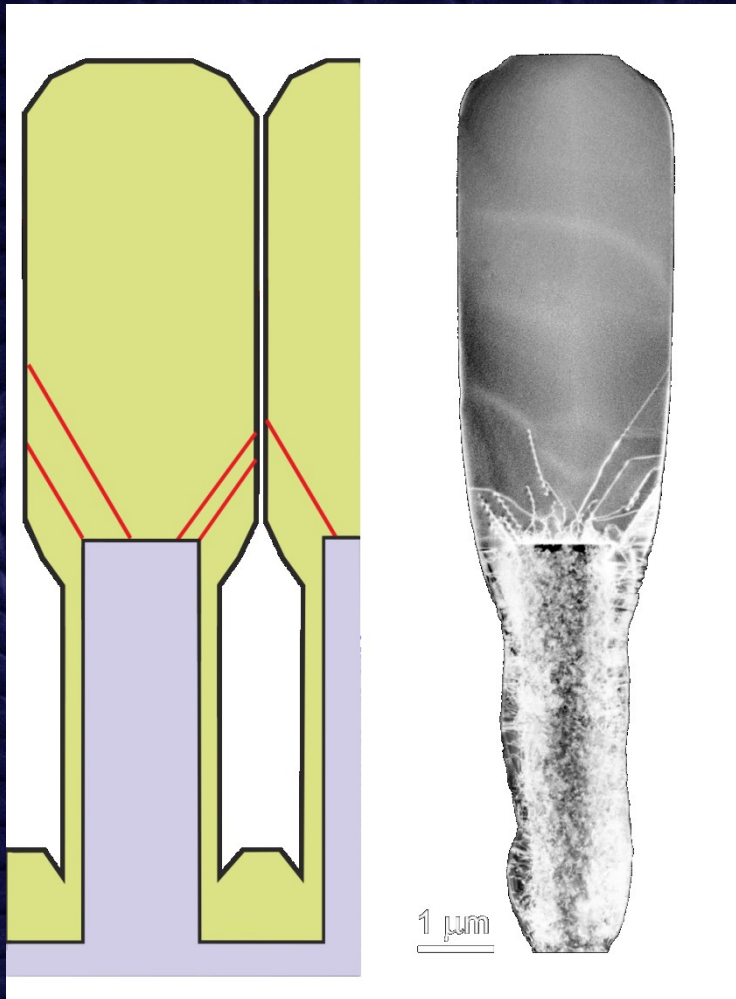
- 1) High deposition rate
(~ 4 nm/s)
⇒ Short diffusion length
- 2) Mutual flux shielding



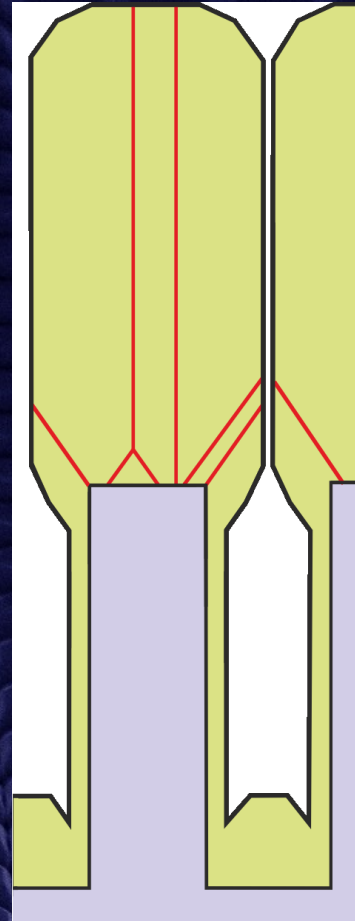
Modelling based on the rate equation for the adatom phase



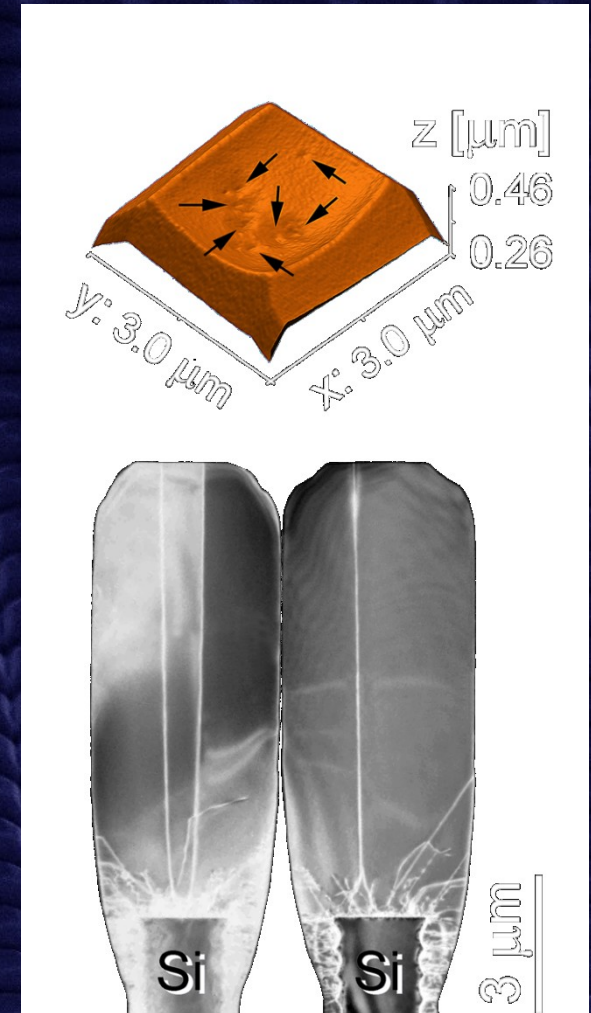
Dislocation Management by Epitaxial Necking



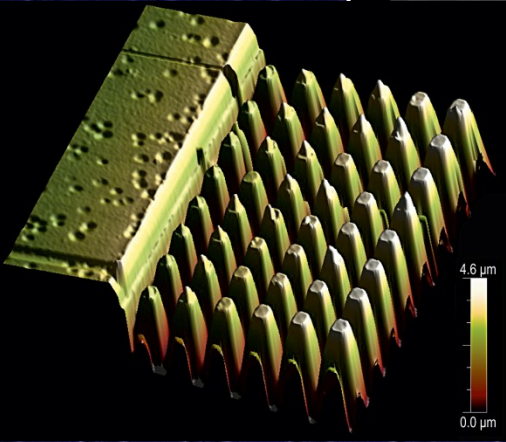
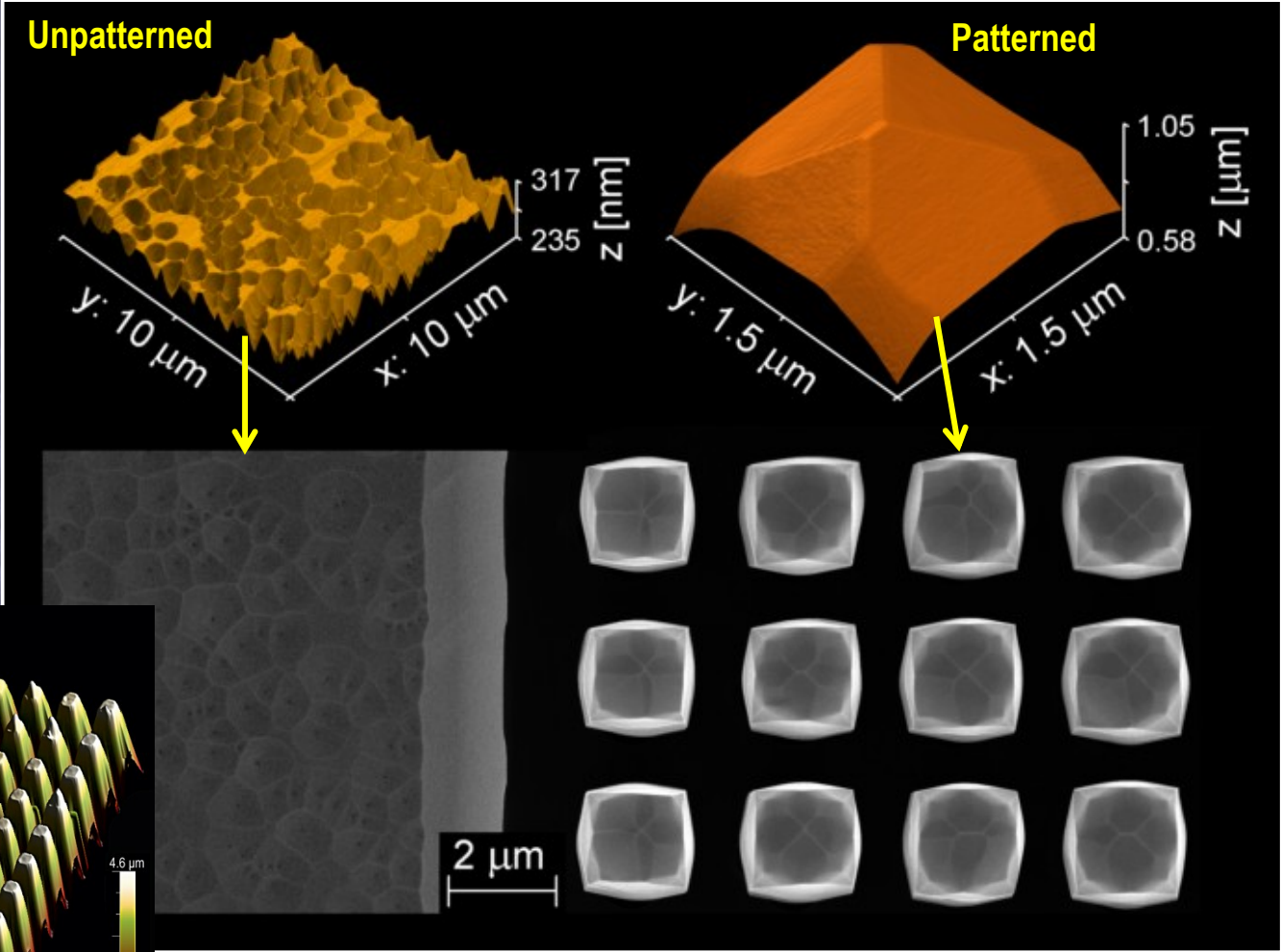
“60° Dislocations”



“Growth Dislocations”



Dislocation Management by Surface Facetting

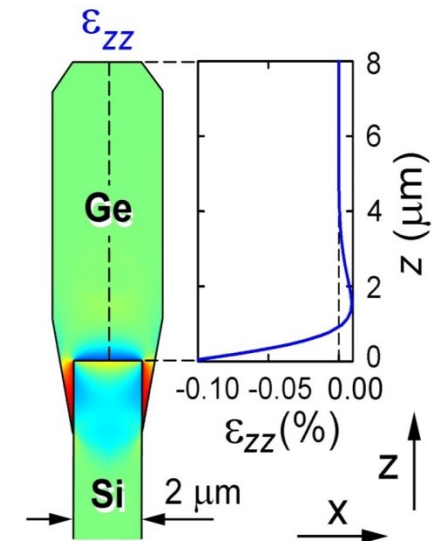
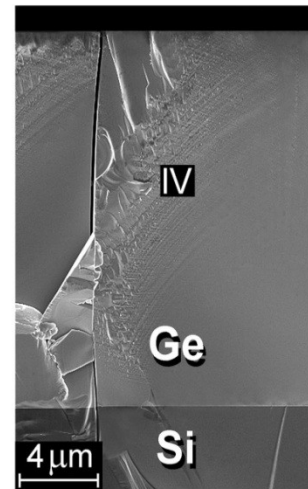
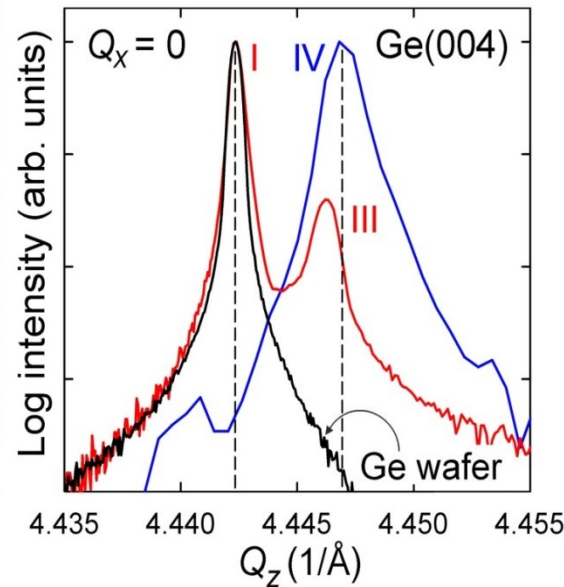
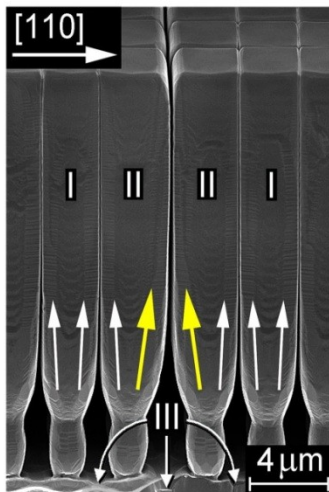
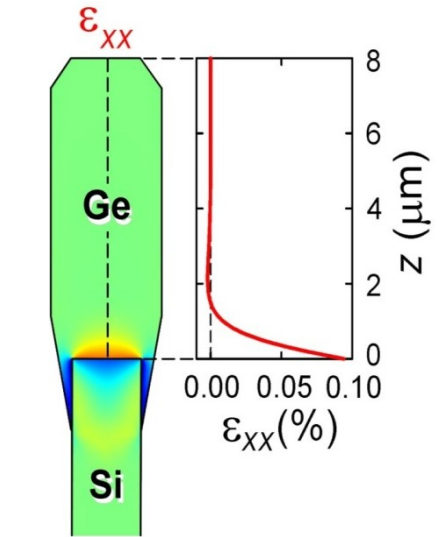
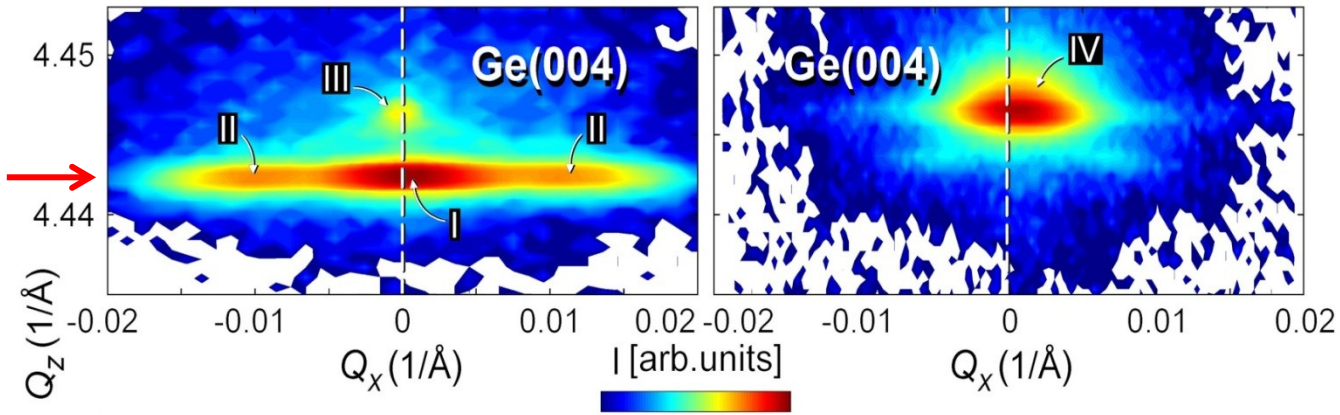


Assessment of the crystalline quality (HRXRD)

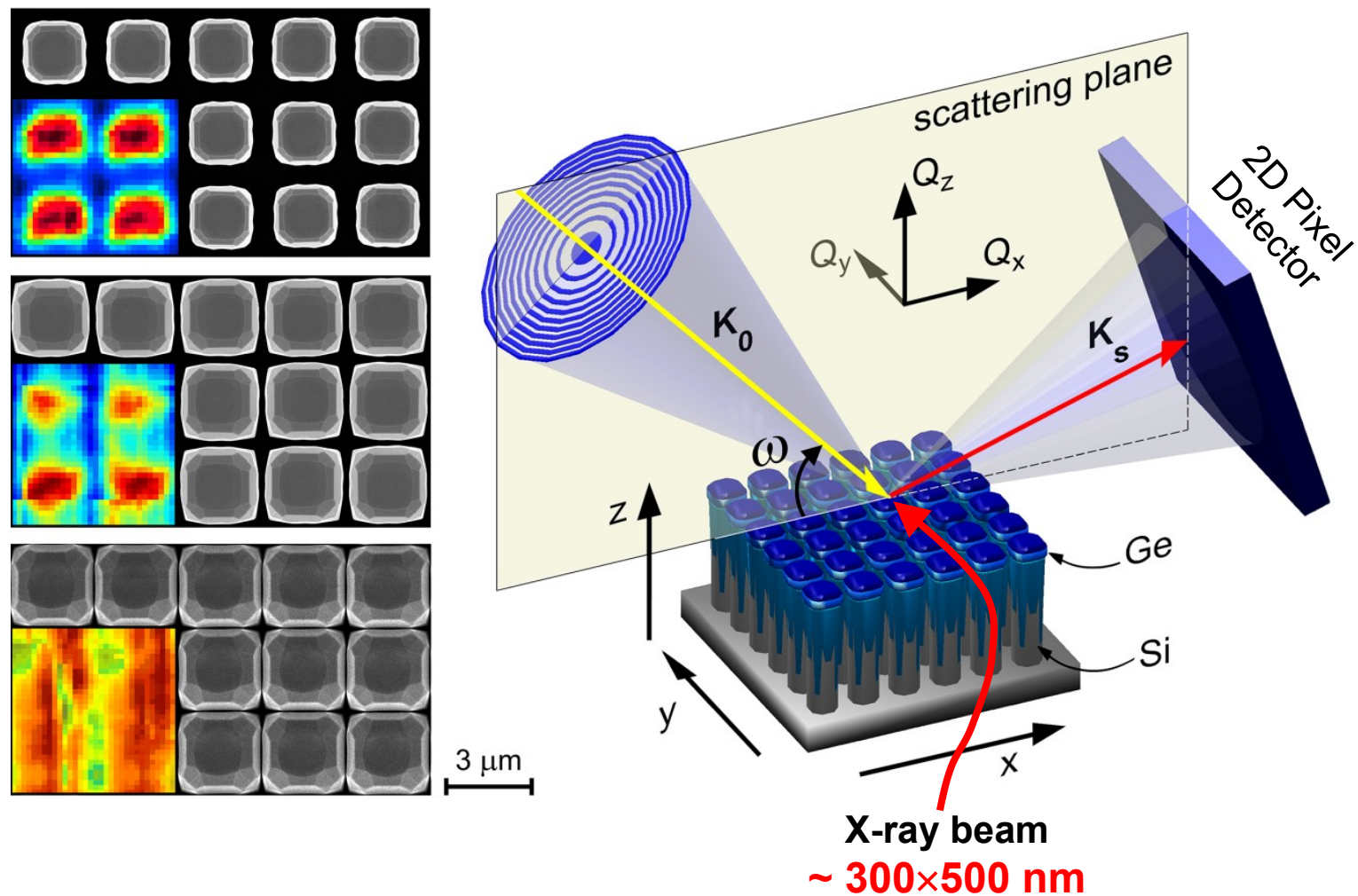
16 μm tall Ge towers

Continuous Film

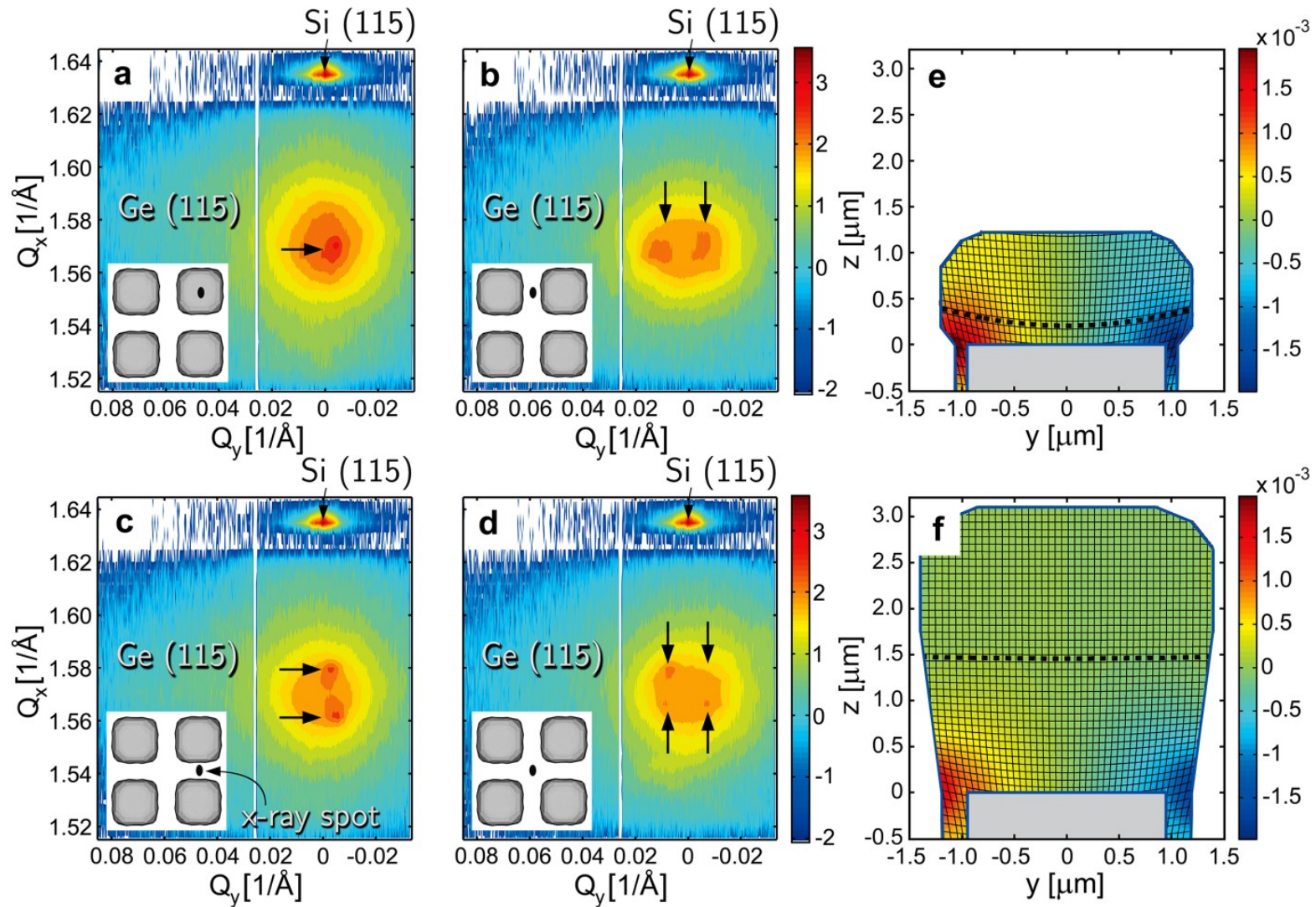
100% relaxed Ge



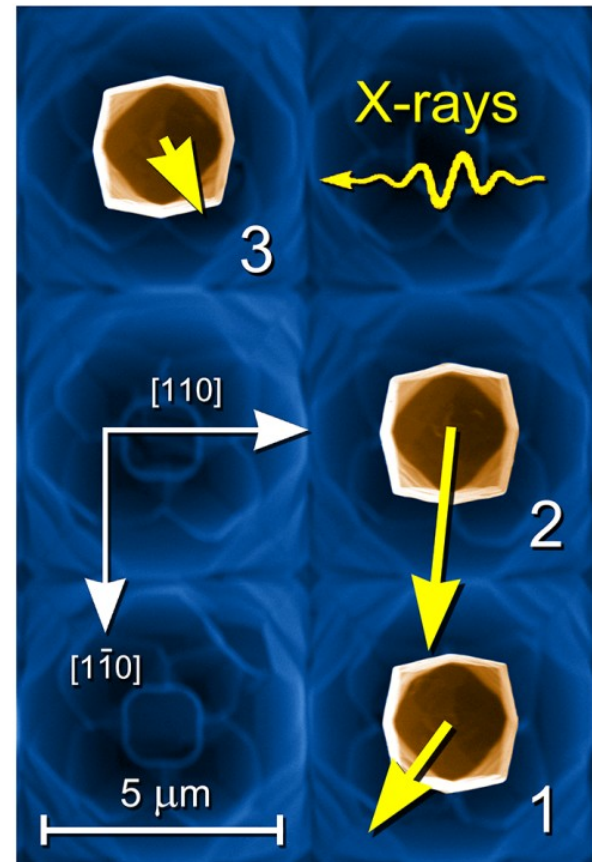
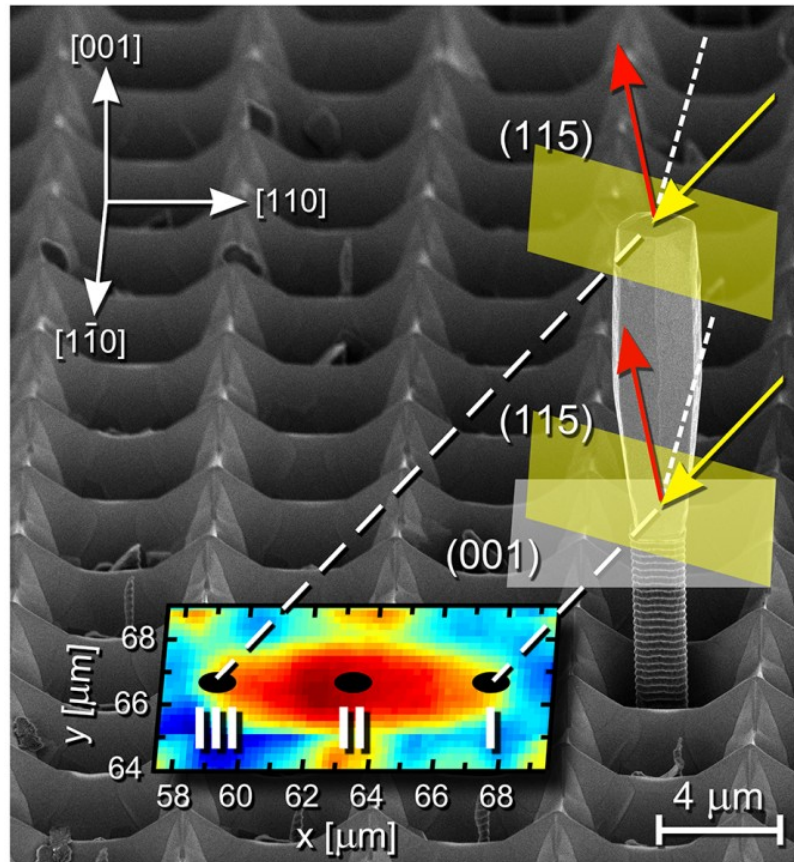
Three Dimensional Nanodiffraction of Ge Crystals



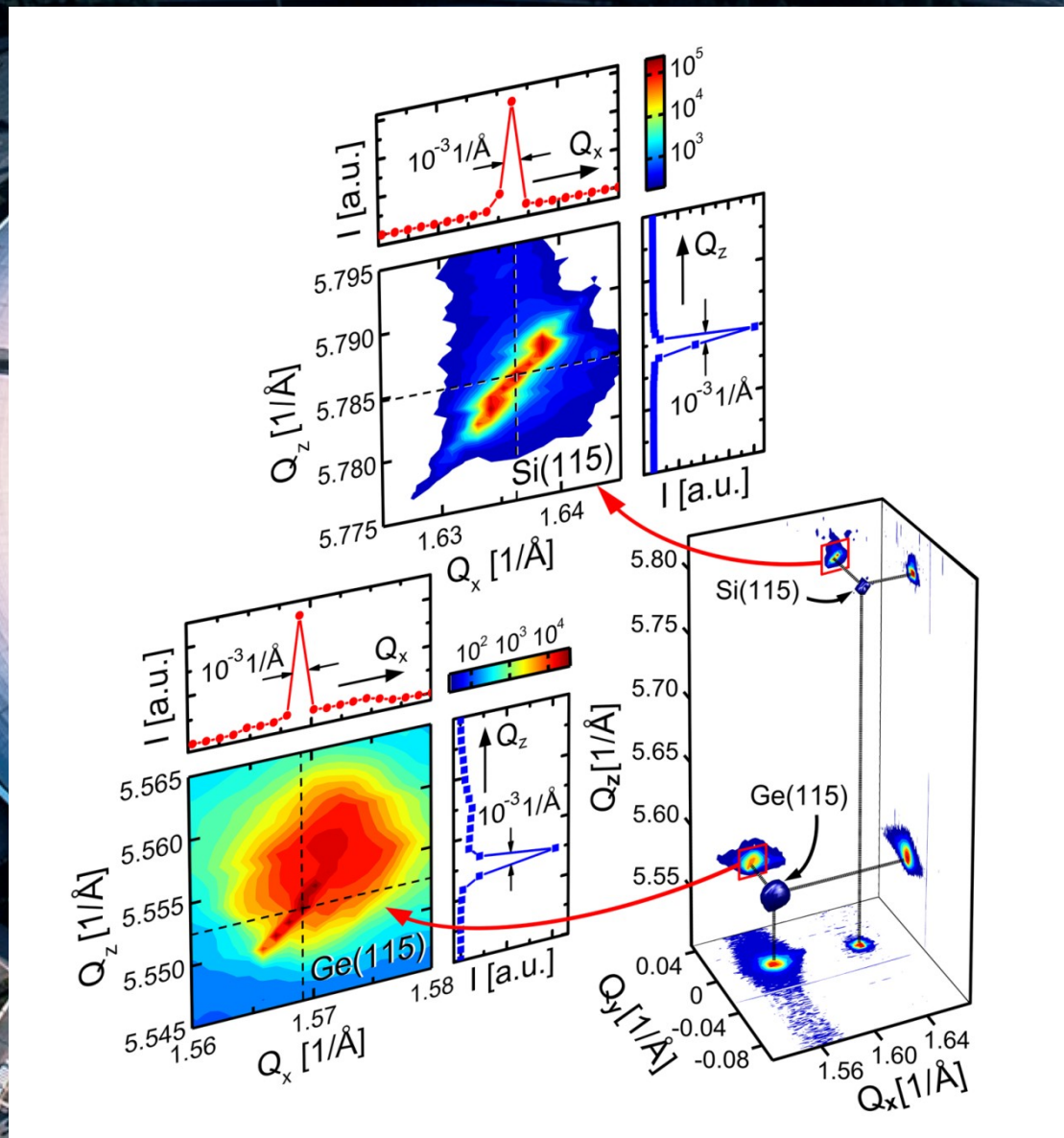
Three Dimensional Nanodiffraction of Ge Crystals

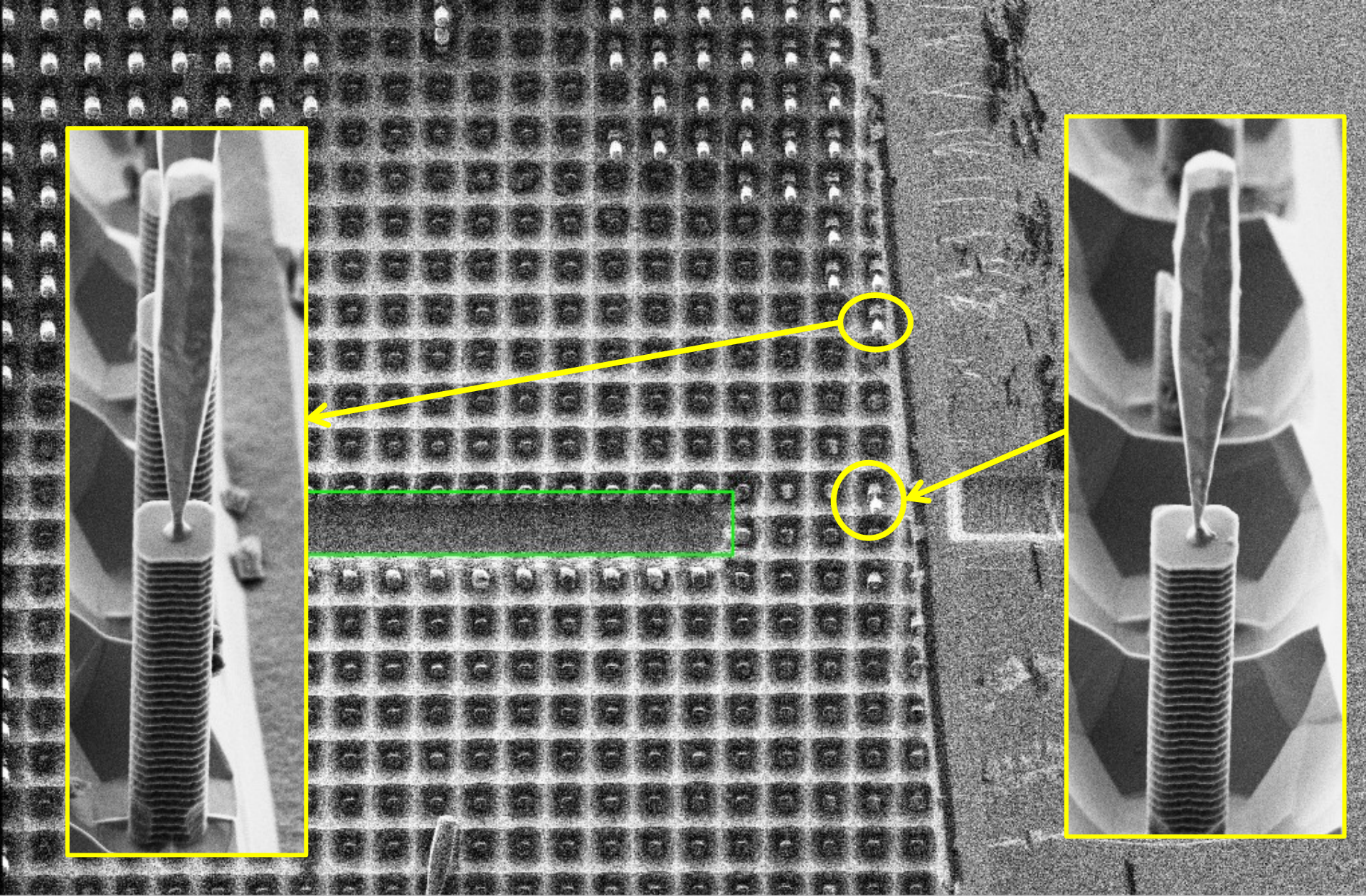


Nanodiffraction of Isolated Ge Crystals



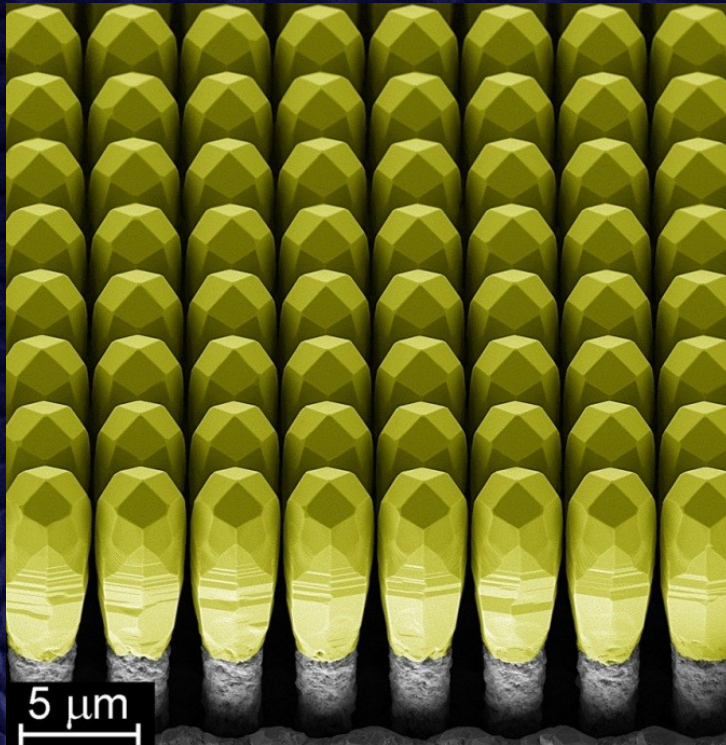
Nanodiffraction of Isolated Ge Crystals





Mag = 1.53 K X	WD = 4.7 mm	Noise Reduction = Pixel Avg.	Stage at T = 45.0 °	FIB Lock Mags = No	Date :21 Sep 2012
10 μm	EHT = 3.00 kV	Scan Speed = 5 N = 1	Tilt Corr. = Off	FIB Probe = 30KV:10 pA	Time :12:17:14
	Signal A = SE2	Width = 183.9 μm	Tilt Angle = 0.0 °	FIB Imaging = FIB	

Strain and Dislocations Free Ge Crystals



TUNABLE QUASIPERFECT MATERIALS !

- Unique in the last 40 years of epitaxial work !
- Until recently considered to be impossible !

Scaling Hetero-Epitaxy from Layers to Three-Dimensional Crystals

Claudiu V. Falub *et al.*

Science **335**, 1330 (2012);

DOI: 10.1126/science.1217666

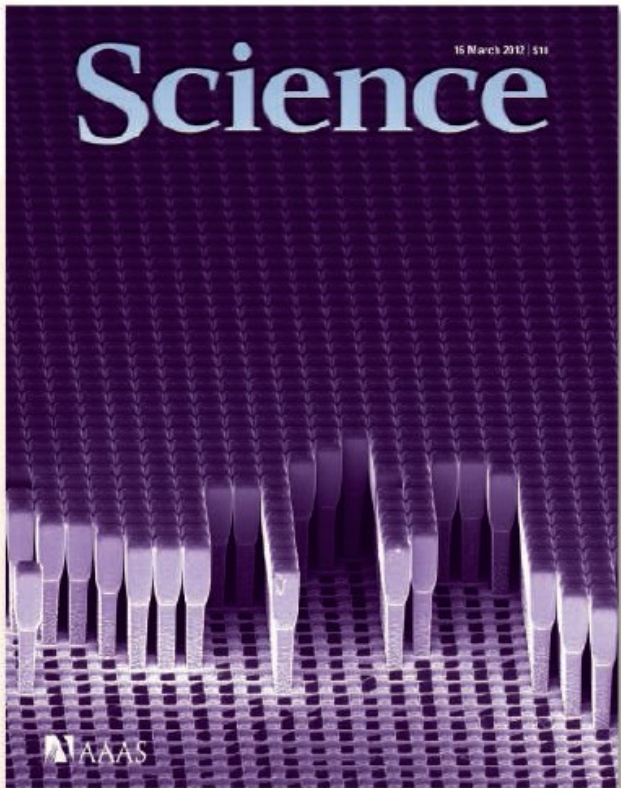
SCIENCE VOL 335 16 MARCH 2012

Published by AAAS

8 December 2011; accepted 27 January 2012

www.sciencemag.org

Terracotta Army (China), 3rd century BC



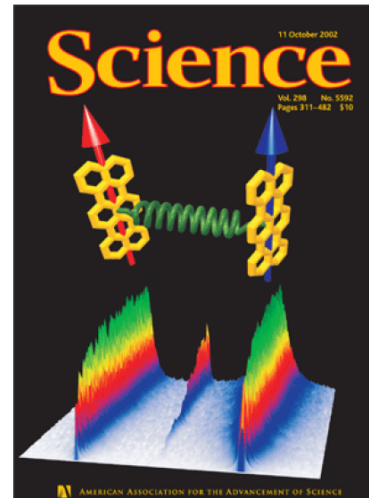
COVER

False-colored scanning electron micrograph of ~8-micrometer-tall germanium crystals, separated by finite gaps, grown onto silicon pillars. In structures like this one, wafer bowing and layer cracking are absent, allowing single-crystal integration of different materials onto a silicon substrate, which serves as a platform for many applications, such as multiple-junction solar cells, x-ray and particle detectors, or power electronic devices. See page 1330.

Image: Claudiu V. Falub, Laboratory for Solid State Physics, Swiss Federal Institute of Technology (ETH-Zürich)



26 July 2002
(Biology)

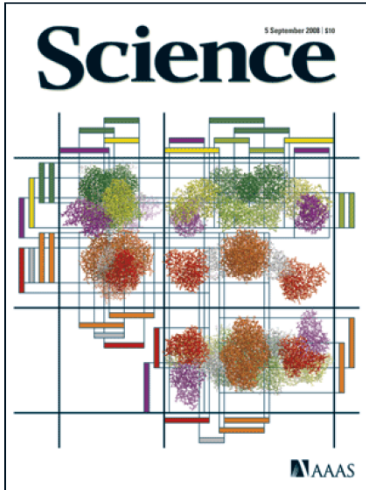


11 October 2002
(Chemistry)

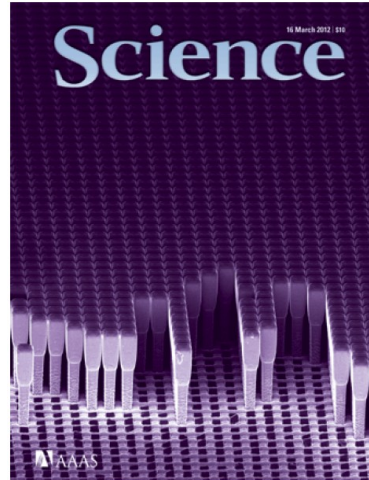
4 ETHZ Science covers (1880-2012)

COVER Electron micrograph showing a stalled DNA replication fork in a checkpoint-defective yeast mutant. Although DNA replication is one mechanism postulated to contribute to genome instability, the molecular basis of genome instability and its role in cancer development are the subject of a special feature in this issue. See page 599 [Image: J. M. Sogo et al.]

COVER Artist's impression of two nearby molecules that "couple" (upper image), lose their individualities, and give rise to new spectral features (lower image). By combining high-resolution laser spectroscopy and scanning probe techniques, it is now possible to look deep into the world of fluorescent molecules even if they are only a few nanometers apart. See page 385 [Image: C. Hettich]



5 September 2008
(Biology)



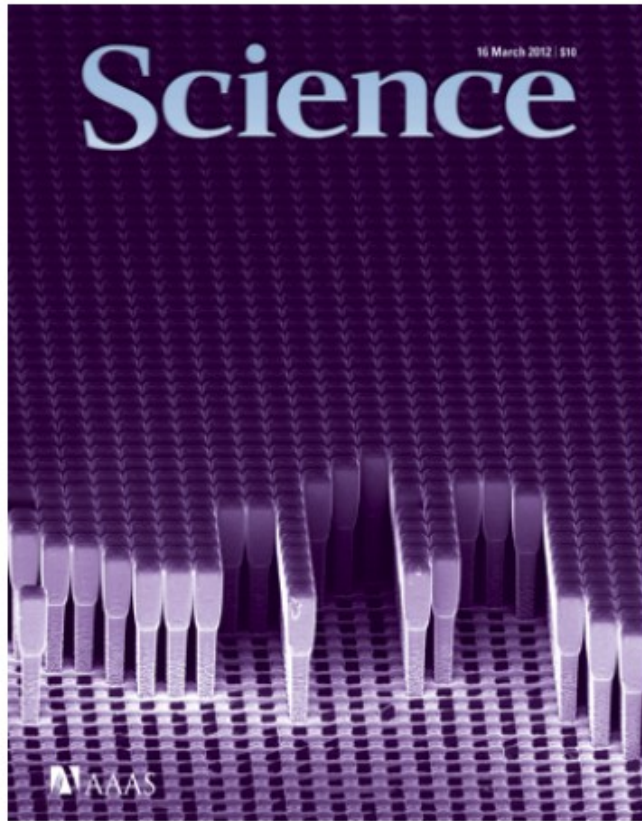
16 March 2012
(Physics / Materials Science)

COVER Mammalian fatty acid synthase, a multienzyme that catalyzes all steps of fatty acid biosynthesis. A blueprint of its atomic structure is shown in three views, and the extent of its functional domains is indicated by colored bars. The versatile segmental construction is also used in other members of this large family of multienzymes, which synthesize natural products such as antibiotics. See page 1315. Image: Marc Leibundgut and Timm Maier/ETH Zurich

COVER False-colored scanning electron micrograph of ~8-micrometer-tall germanium crystals, separated by finite gaps, grown onto silicon pillars. In structures like this one, wafer bowing and layer cracking are absent, allowing single-crystal integration of different materials onto a silicon substrate, which serves as a platform for many applications, such as multiple-junction solar cells, x-ray and particle detectors, or power electronic devices. See page 1330. Image: Claudiu V. Falub, Laboratory for Solid State Physics, Swiss Federal Institute of Technology (ETH-Zürich)

COVER Low-energy (<10 millielectron volts) electronic spectra of bilayer graphene undergoing nematic phase transition from an isotropic, unperturbed form (top left) to an asymmetric form (bottom right). Electron-electron interactions in suspended graphene layers drive this transition, causing a change in the material's band structure and, thus, its electronic properties. See page 860. [Image: [Kostya S. Novoselov/University of Manchester](#) and Yael Fitzpatrick/Science]

16 March 2012



COVER False-colored scanning electron micrograph of ~8-micrometer-tall germanium crystals, separated by finite gaps, grown onto silicon pillars. In structures like this one, wafer bowing and layer cracking are absent, allowing single-crystal integration of different materials onto a silicon substrate, which serves as a platform for many applications, such as multiple-junction solar cells, x-ray and particle detectors, or power electronic devices. See page 1330. Image: Claudiu V. Falub, Laboratory for Solid State Physics, Swiss Federal Institute of Technology (ETH-Zürich)

12 August 2011



International Reactions

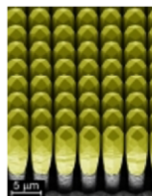


ETH Zurich Produce Se
 Published on March 20, 2012
 By Andy Choi

A new manufa- structures mac developed by s Università di M The semicondu thickness on si found in abund

12 March 2012
 Swiss researche for applications
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 'You would end u mismatch, beau and night,' said

Sono i soldi che fanno le delle belle (Federico Fellini)



Perspective scanning electron Silicon pillars.

The novel structures compris production of these structure method and is followed by etg growing the structure over th the crystals. This results in th team, has produced defect-fr achieved.

The method prevents the forr considerably reduces the ben thermal coefficients of expan critical layer cracking, resultir

The semiconductor structures and power electronics. One in absorbers. The new approach sector. The method is also su applications. It substitutes he the approach offers significan

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 First for techn
 Composite
 MARCH 2012
450mm Wafer Transition P. 13
Metrology Beyond 22nm P. 16
Copper Wire Bonding P. 23

March 16, 2012
 Milano-Bicocc
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Image: Perspe electron micro an 8um-tall ar germanium crys silicon pillars, Zurich.

The researche manufacturing thermal expan integrations wi

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This **silicon-ge** similar results.

Applications f with ultra-high cells; and pow

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Merging incompatible materials

edp sciences

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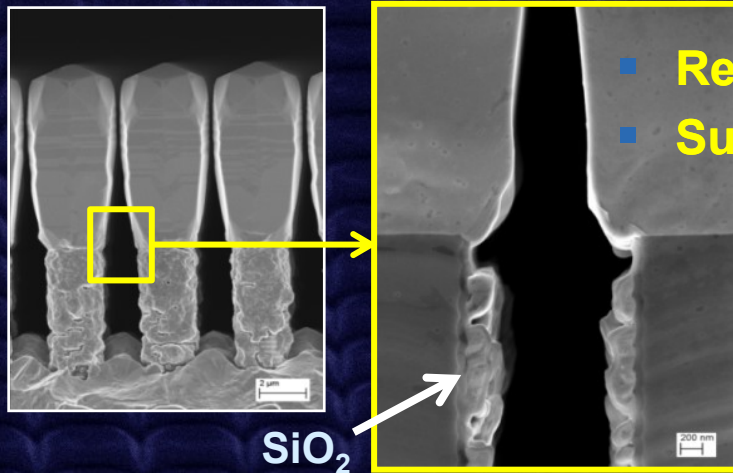
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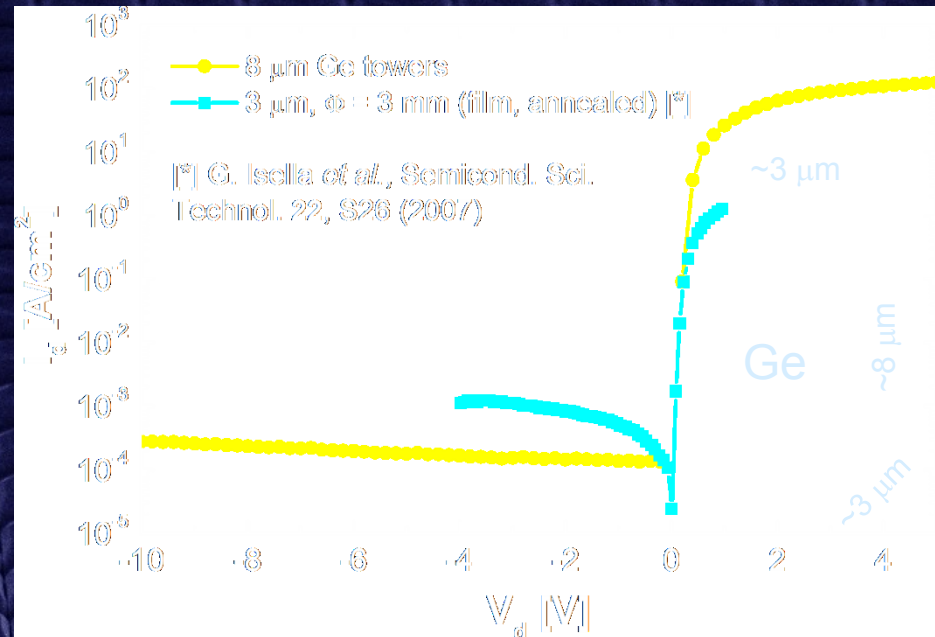
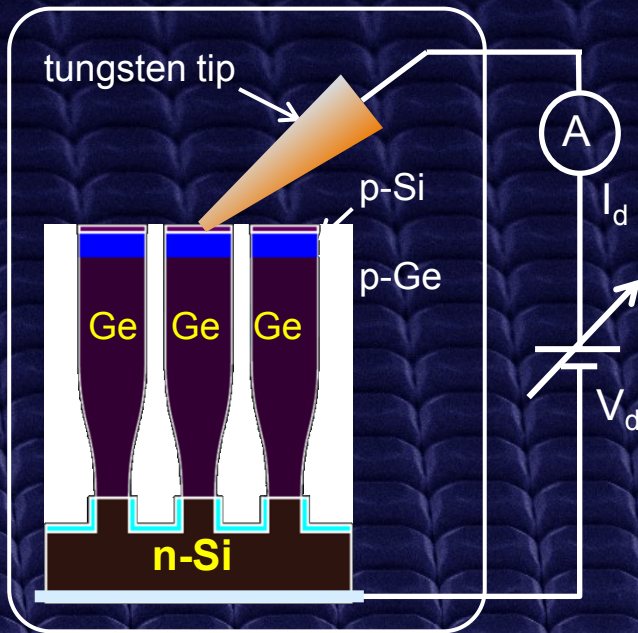
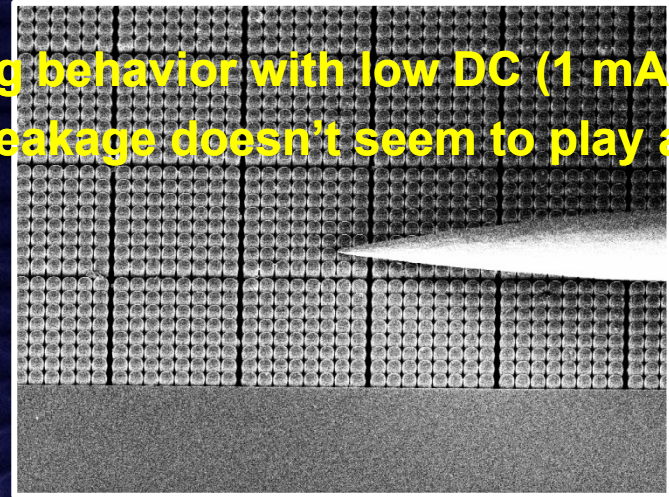
...edge they have gained through their work with
 ...earchers expect that the technology will be
 ...r materials such as gallium arsenide or silicon



Electrical Measurements



- Rectifying behavior with low DC (1 mA/cm²)
- Surface leakage doesn't seem to play a role



Electrical Measurements

2 μm tall Ge diodes grown on 2 μm tall Si pillars	As grown	After etching in H_2O_2	
	$4 \times 4 \mu\text{m}^2 \diamond$	$4 \times 4 \mu\text{m}^2 \diamond$	$8 \times 8 \mu\text{m}^2 \clubsuit$
I_d [nA] *	28900	0.47 ± 0.19	7.3 ± 4.4
I_d [mA/cm ²] *	172.2×10^3	2.8 ± 1.1	12.0 ± 7.2
n	1.7	1.11 ± 0.03	1.19 ± 0.04
R_s [k Ω]	4.7	21.6 ± 12.3	17.1 ± 9.4
R_p [G Ω]	2.45×10^{-5}	1.26 ± 0.35	0.18 ± 0.08

(* Measured at $V_d = -1$ V; \diamond Area (SEM): $16.8 \mu\text{m}^2$; \clubsuit Area (SEM): $62.4 \mu\text{m}^2$)

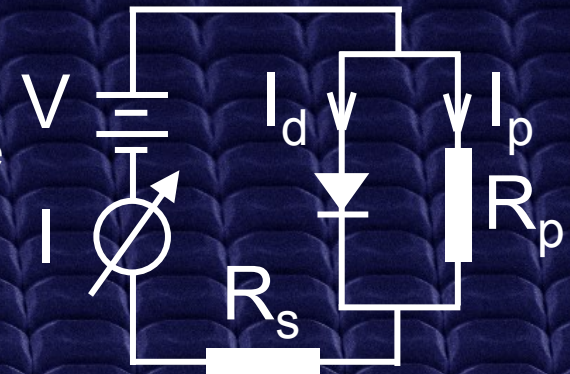
$$I_d + I_p = I_s \left(\exp \left(\frac{\beta}{n} (V - IR_s) \right) - 1 \right) + G_p (V - IR_s)$$

I_d , I_s diode and saturation currents; $\beta = e/kT$, R_s series resistance

$R_p = 1/G_p$ (G_p parallel conductance); n – ideality factor

I_p shunt current, I_s , n , R_s , $G_p \neq f(V)$

* J.H. Werner, Appl. Phys. A 47, 291 (1988)



From Ge crystals to NEXRAY xensors

DEVICE PROCESSING (in progress)

- 8" CMOS wafer
- Thinned down to 100 μm
- Patterned (DRIE)
- Pillars passivation
- 50 μm Ge towers (backside)
- Sealing of the Ge towers
- Ge etching
- Electrode depositi
- Dicing

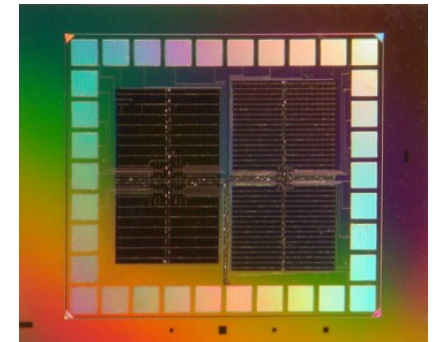
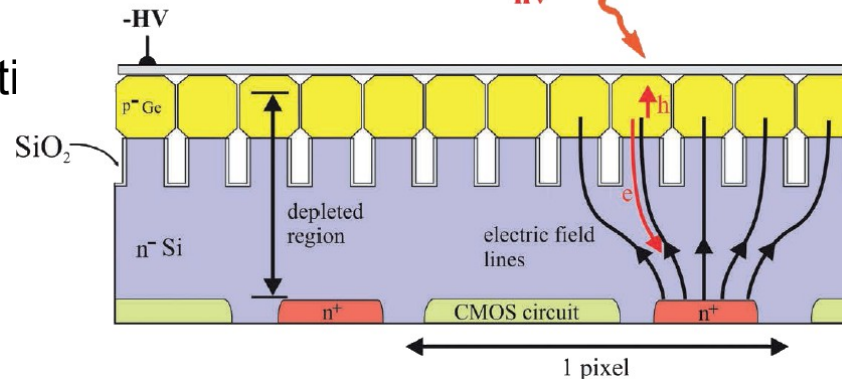
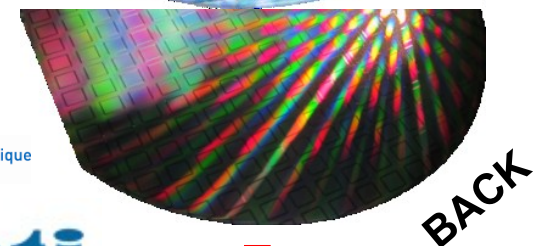
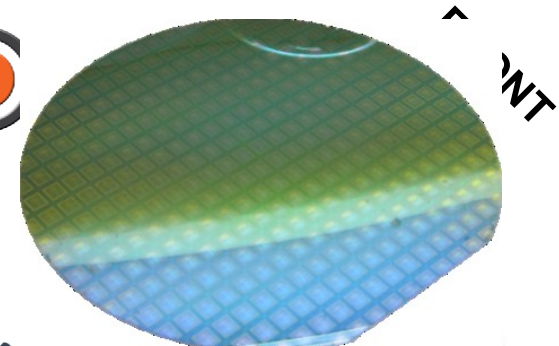
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ETH
Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

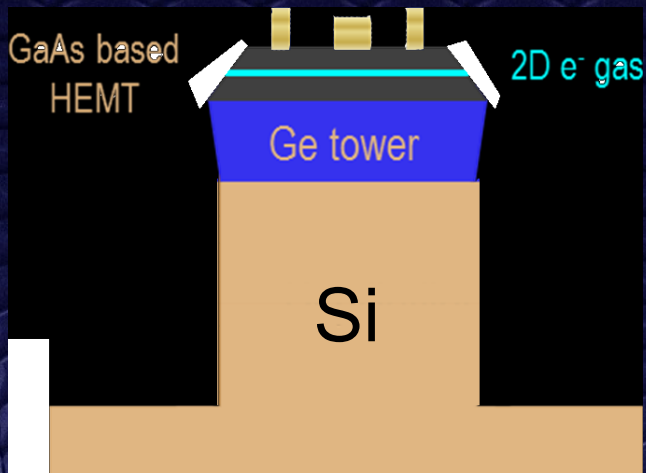
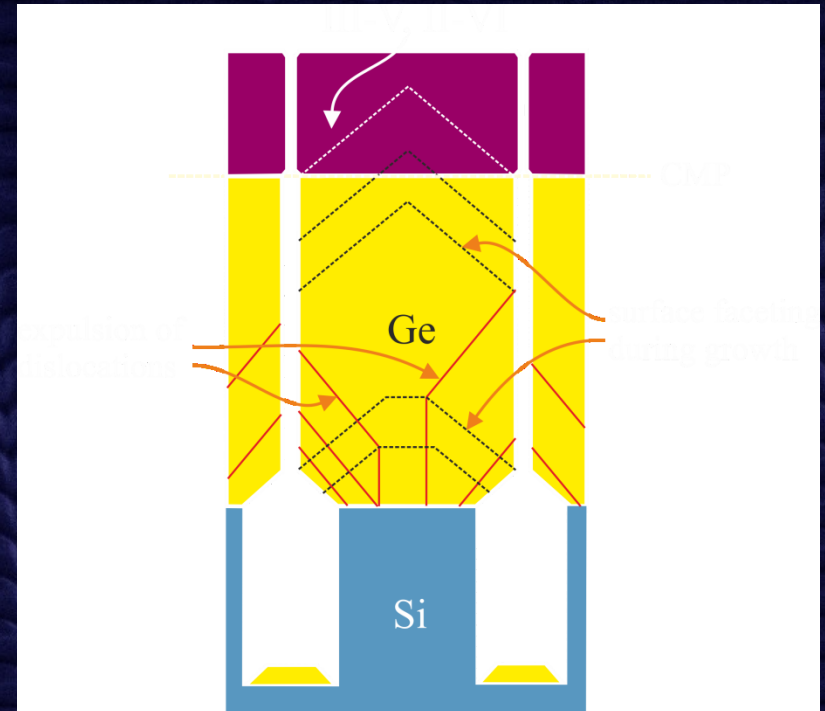
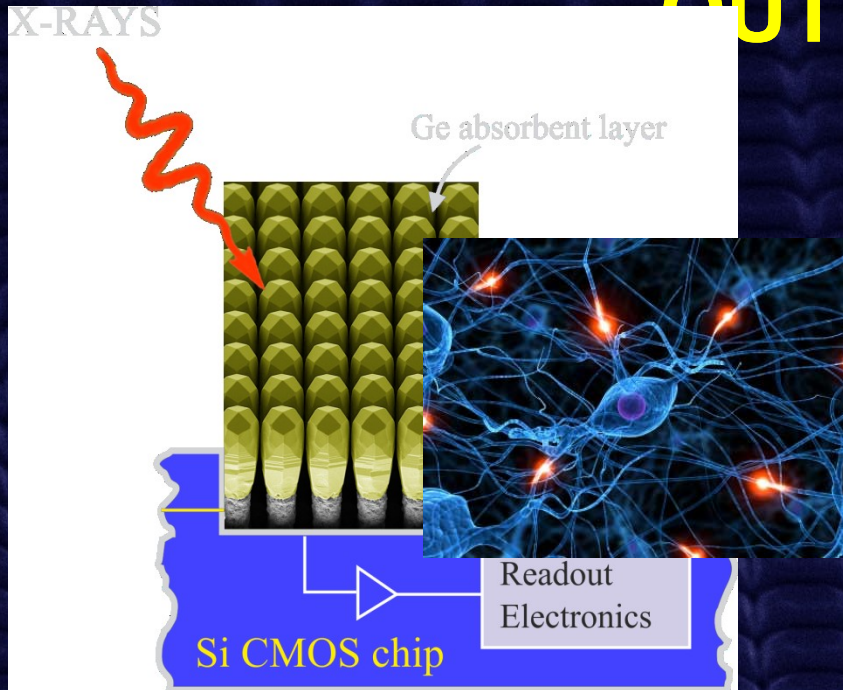
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et de microtechnique

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OUTLOOK



- III-V, II-VI integration with CMOS
- High-efficiency solar cells
- Power electronic devices (SiC on Si)
- Emitters, resonators, etc.

OUTLOOK

Integration of III-V optoelectronic devices on Si substrates

Si

- Small mass
- Good thermal conductivity
- Large wafer diameter
- Mainstream Technology

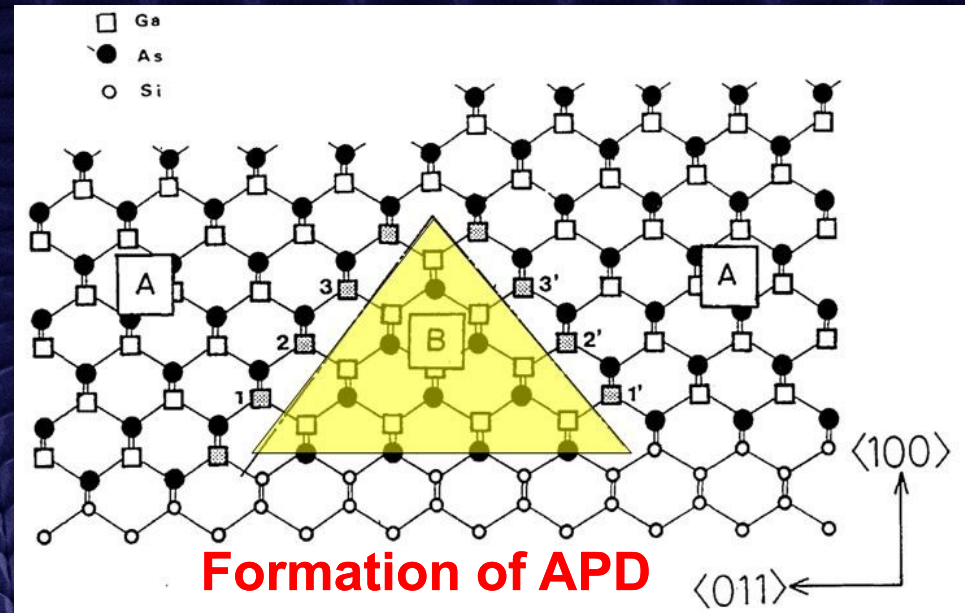
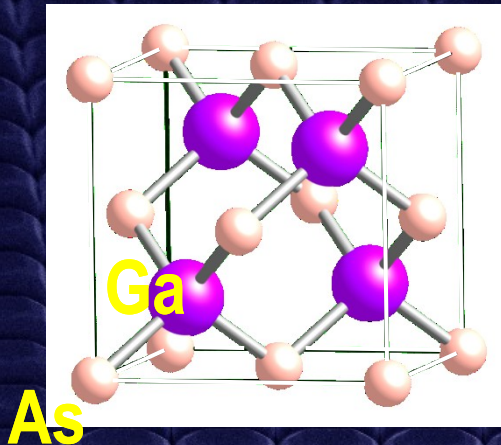
GaAs

- direct band gap alignment
- high carrier mobility
- Optimum for the development of optoelectronic devices

CHALLENGES

- 4% lattice mismatch
- 60% thermal mismatch
- Anti Phase Domains (APD)

GaAs unit cell



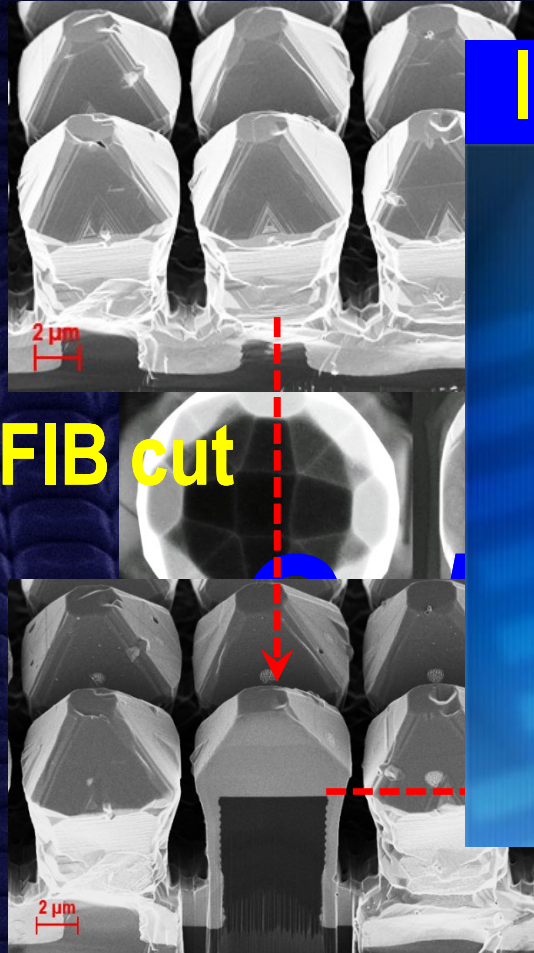
OUTLOOK

Integration of III-V optoelectronic devices on Si substrates

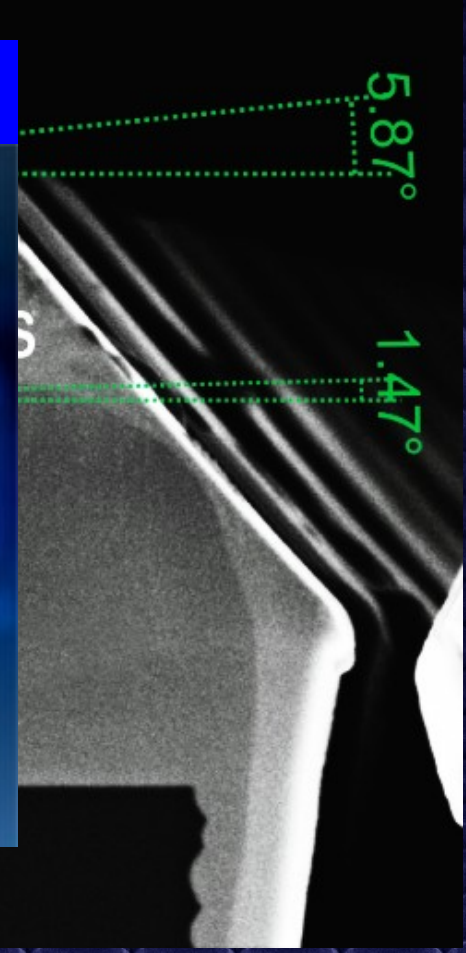
Si

GaAs

CHALLENGES



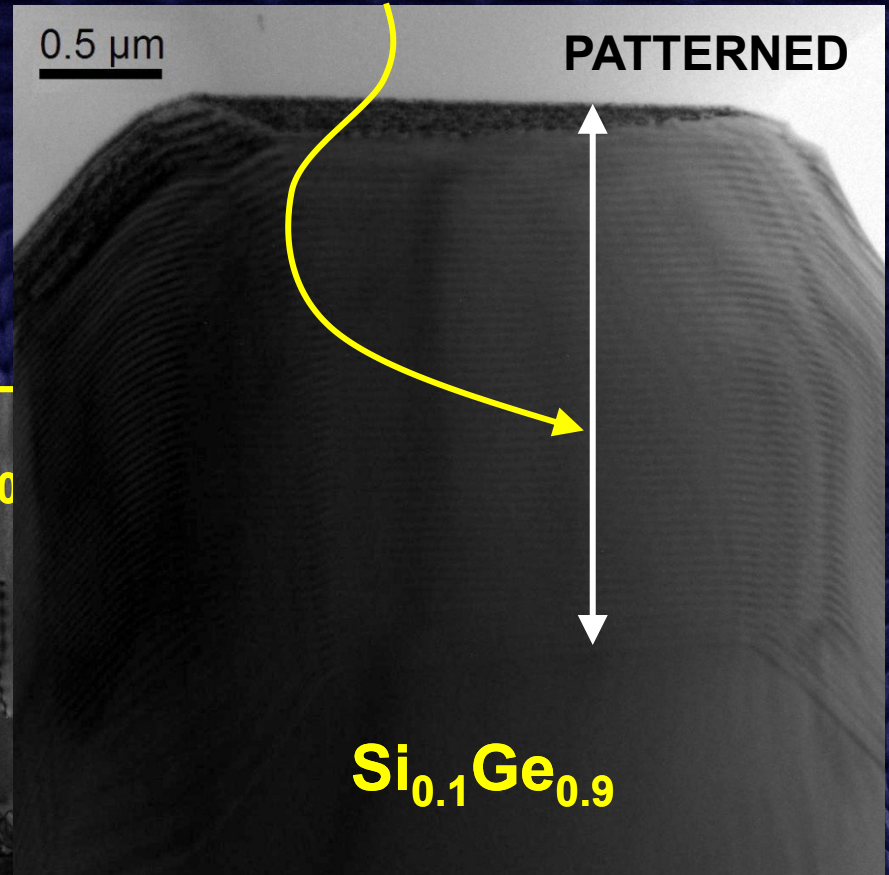
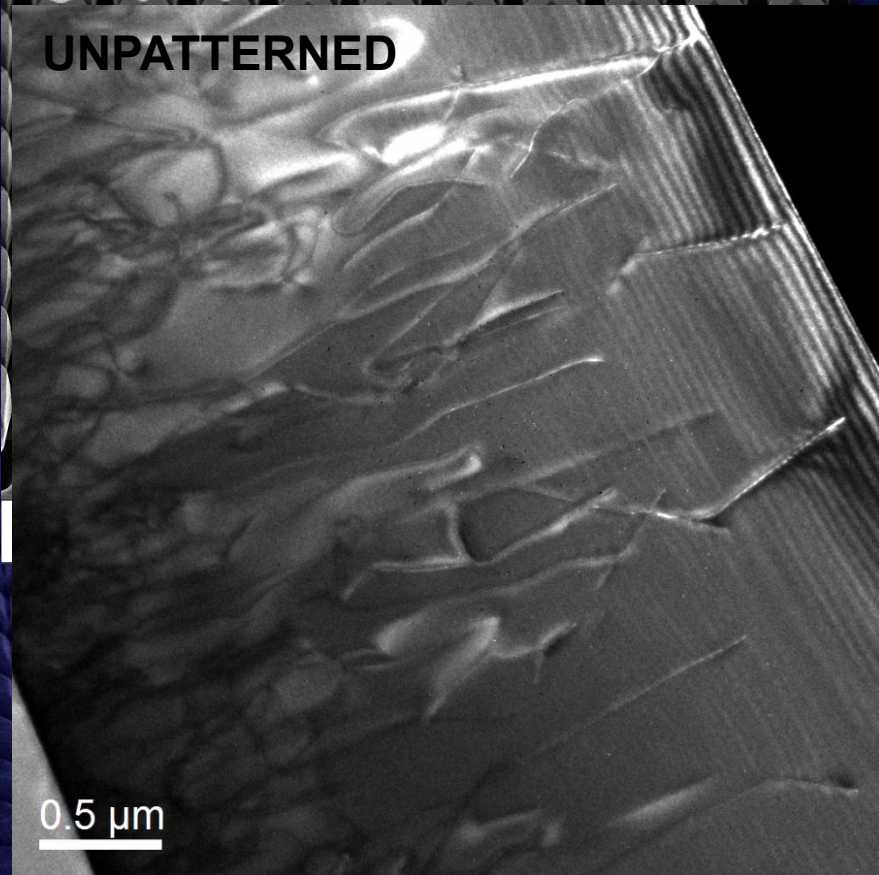
In collaboration with



OUTLOOK

Integration of SiGe MQW

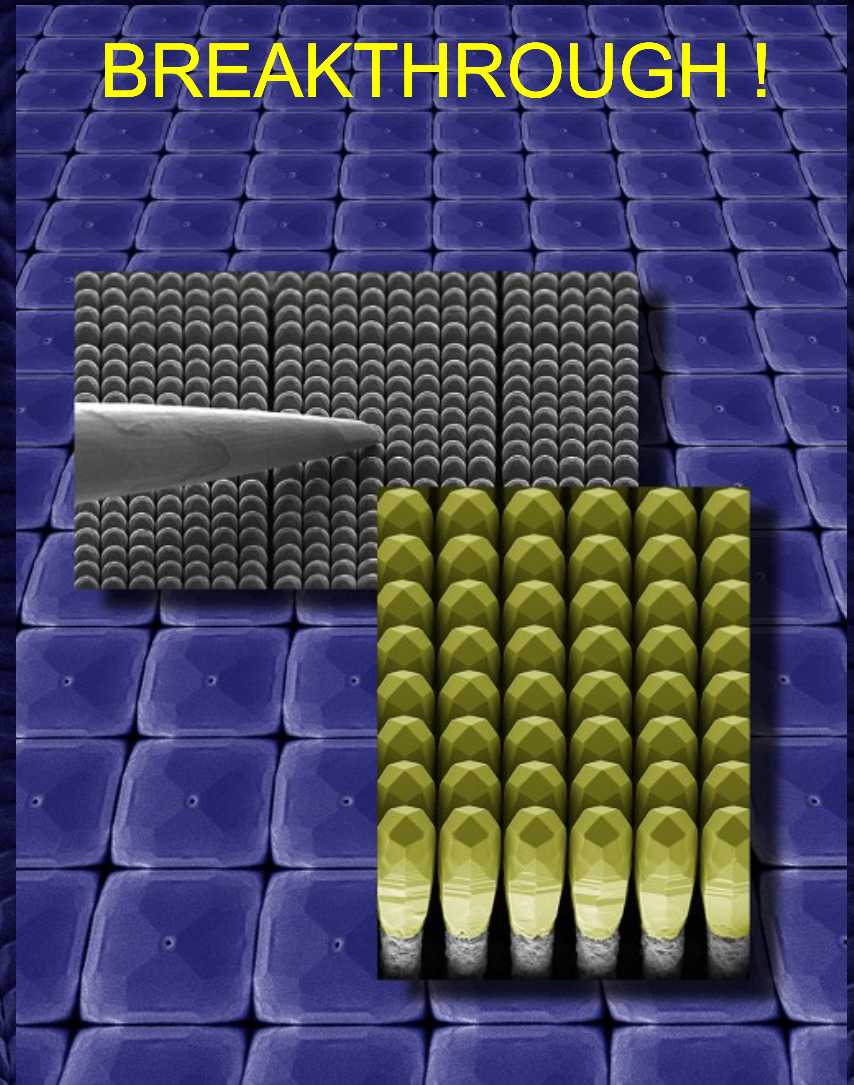
50× (21 nm $\text{Si}_{0.1}\text{Ge}_{0.9}$ + 10 nm Ge)



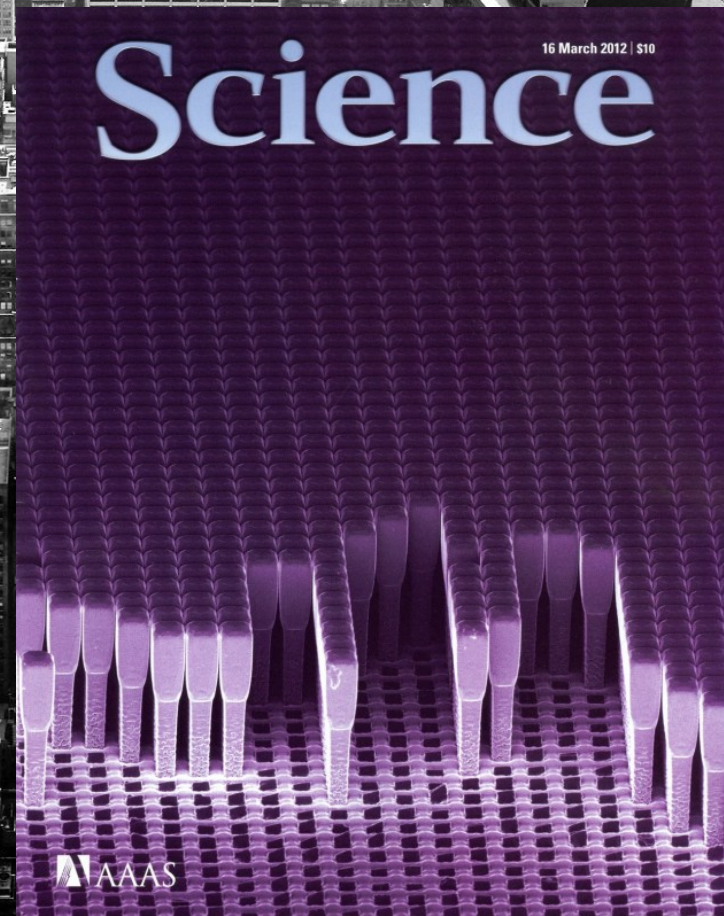
0.5 μm

SUMMARY

- 3D Heteroepitaxial growth of lattice and thermally mismatched semiconductor systems on clean, patterned substrate surfaces:
 - **allows dislocation management;**
 - **avoids wafer bending;**
 - **avoids layer cracks.**
- Applicable to wide range of layer thicknesses.
- Diode characteristics (i.e. low DC).
 - **Monolithically integrated X-ray detector (high resolution & sensitivity).**
- Applicable to other systems (III-V, etc).
- Allows novel applications & devices (e.g. emitters, resonators, etc.)



ACKNOWLEDGMENTS



Thank you for your attention !

Khumjung, Himalaya, Nepal, November 2003